MEMC®

TECHNOLOGY IS BUILT ON US

# CORPORATE FACT SHEET
SEPTEMBER 2006
NYSE: WFR

## MARKET SNAPSHOT
## JUNE 30, 2006

| | |
|---|---|
| 52-week Range | $16.43 - $48.75 |
| Market Capitalization | $8.6 billion |
| Average Daily Volume | 3,924,343 |

## COMPANY PROFILE

MEMC is a global leader in the manufacture and sale of wafers and related intermediate products to the semiconductor and solar industries. Wafers are the fundamental building blocks that virtually all semiconductor devices and solar cells are built upon. The company has been a pioneer in the design and development of wafer technologies for more than four decades and maintains R&D and manufacturing facilities in Europe, Japan, Korea, Malaysia, Taiwan and the United States. Utilized by leading semiconductor and solar makers worldwide, MEMC's products are the foundation for devices that enable the internet and electronic commerce, computers, cell phones, consumer electronics, automobiles, industrial automation and control systems, telecommunications, defense systems and solar power.

## COMPANY STRATEGY

MEMC aims to strengthen its leadership position through a continuous improvement of technology, market share and profits.

## FINANCIAL HIGHLIGHTS

(in thousands, except per share data and percentages)

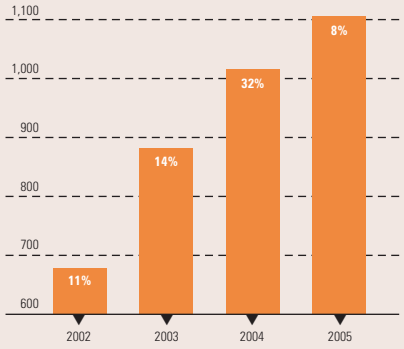
| Year ended December 31, | 2005 | 2004 | 2003 |
|---|---|---|---|
| Net sales | $1,107,379 | $1,027,958 | $781,100 |
| Gross margin | $ 366,518 | $ 369,415 | $232,756 |
| Operating income | $ 256,993 | $ 260,488 | $142,619 |
| Net income | $ 249,353 | $ 226,201 | $116,617 |
| Basic income per share | $ 1.17 | $ 1.09 | $ 0.58 |
| Diluted income per share | $ 1.10 | $ 1.02 | $ 0.53 |
| Cash flows from operating activities | $ 320,996 | $ 258,027 | $ 98,736 |
| Free cash flow[1] | $ 158,258 | $ 112,187 | $ 31,340 |
| Return on assets[2] | 23% | 26% | 17% |

(1) Cash flows from operating activities minus capital expenditures.
(2) Net income divided by average of beginning and ending total assets.

**MEMC** TECHNOLOGY IS BUILT ON US
**501 Pearl Drive (City of O'Fallon) • P.O. Box 8 • St. Peters, MO 63376**
**(636) 474-5000 • www.memc.com**

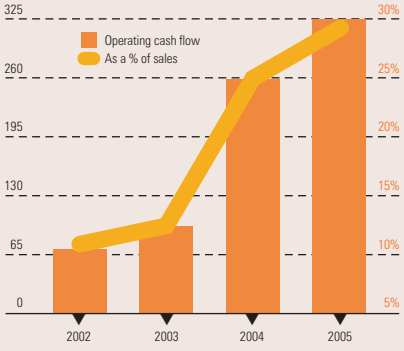
### MEMC REVENUE

With year over year growth rates, (Dollars in millions)

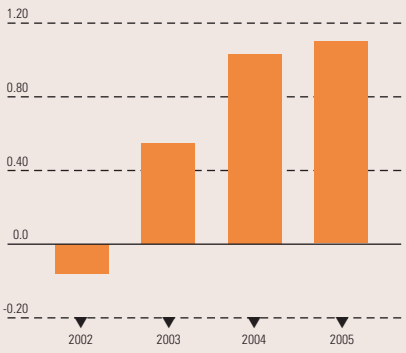



### OPERATING CASH FLOW

With percentage of sales




### DILUTED EARNINGS PER SHARE



## BROAD PRODUCT PORTFOLIO AND INTELLECTUAL PROPERTY PORTFOLIO

MEMC's product portfolio is currently the broadest in the company's history. Two of our more recent innovations are Magic Denuded Zone® wafer technology and defect-free crystal, both of which we have incorporated in our OPTIA™ product line. These two innovations are designed to help our customers improve the yield and capability of their semiconductor fabrication processes.

MEMC holds over 500 worldwide patents on silicon products and processes and has over 350 worldwide patent applications on file, including patents on MDZ® and OPTIA™.

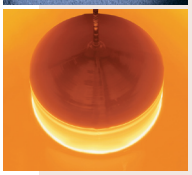
## RICH HERITAGE

Founded in 1959, MEMC was the first merchant manufacturer of wafers and was first to produce…

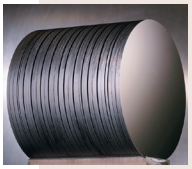
- **Chemical-Mechanical Polishing of Silicon** MEMC was the first to develop chemical-mechanical polishing for the semiconductor industry in 1962.
- **Commercial epitaxial wafers** MEMC pioneered epi wafers for CMOS applications in 1982.
- **200mm wafers** MEMC was the first to commercialize 200mm wafers in the mid-to-late 1980s, in partnership with IBM.
- **Granular Polysilicon** MEMC developed the first process using granular polysilicon in 1991.
- **Magic Denuded Zone® (MDZ®)** MEMC developed this breakthrough thermal processing technique in the mid-1990s and was granted the first patent on this technology in 1999.
- **PerfectSilicon™** MEMC developed the world's first 100% defect-free silicon wafer in the mid-1990s and was granted the first patent on this technology in 1999. This is now marketed as the OPTIA™ product line.

**MEMC continues to remain at the forefront of technology innovation with advanced manufacturing capabilities and a suite of process technologies including alternate materials such as SOI and strained silicon.**

Electronic grade granular polysilicon



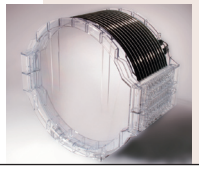
Ingot growth



Wafer slicing

Wafer polishing

Epitaxial deposition



Wafers in shipping cassette

## ENABLING TECHNOLOGY: THE ELECTRONICS FOOD CHAIN

**MEMC Wafers: Technology Is Built On Us**

**Semiconductors**
$230 billion

- Memory
- Foundry/Logic
- Analog
- Processor
- Discrete

**Electronics**
~$1 trillion

*Consumer:*
- Cell phones
- PDAs
- Radios
- CD & DVD players
- Digital cameras

*IT:*
- Computers
- Laptops
- Servers
- Mainframes
- Storage

*Automotive:*
- ABS
- Power steering
- Electric vehicles

*Industrial:*
- Refrigeration
- Motor control

*Aerospace/ Defense:*
- Aircraft
- Ships
- Satellite

## WORLDWIDE LOCATIONS

**Manufacturing Facilities**
- St. Peters, Missouri
- Sherman, Texas
- Pasadena, Texas
- Merano, Italy
- Novara, Italy
- Kuala Lumpur, Malaysia
- Hsinchu, Taiwan
- Chonan, South Korea
- Utsunomiya, Japan

**Sales Offices**
- St. Peters, Missouri
- San Jose, California
- Sherman, Texas
- Paris, France
- Munich, Germany
- Novara, Italy
- Kuala Lumpur, Malaysia
- Shanghai, China
- Hsinchu, Taiwan
- Seoul, Korea
- Tokyo, Japan
- Osaka, Japan

**Contact Information: Bill Michalek • Director, Investor Relations • (636) 474-5443 • www.memc.com**