



MoSys Inc. Corporate Overview

May 2010

Len Perham - CEO
Jim Sullivan - CFO

Safe Harbor Statement



This presentation may contain forward-looking statements about MoSys, including, without limitation, benefits and performance expected from use of its embedded memory and interface technologies and ICs, improving operational efficiencies, the timing of product development and shipments of Bandwidth Engine ICs, anticipated benefits and performance expected from the Bandwidth Engine product, growth in the size of the market addressed by the Company's business and the Company's future markets and future business prospects.

Forward-looking statements are based on certain assumptions and expectations of future events that are subject to risks and uncertainties. Actual results and trends may differ materially from historical results or those projected in any such forward-looking statements depending on a variety of factors. These factors include, but are not limited to the following:

- customer acceptance of our proprietary embedded memory and interface technologies and ICs;
- timing and nature of the license agreements to be entered into with our customers and their requests for our services under existing license agreements;
- timing of customer acceptance of our work under such agreements;
- level of commercial success of licensees' products, ease of manufacturing and yields of devices incorporating our proprietary technologies;
- our ability to enhance our existing proprietary technologies and develop new technologies;
- difficulties and delays in the development, production, testing and marketing of the Bandwidth Engine ICs;
- anticipated costs and technological risks of developing and bringing ICs to market;
- willingness of our manufacturing partners to assist successfully with the fabrication of Bandwidth Engine ICs;
- availability of quantities of ICs supplied by our manufacturing partners at a competitive cost;
- our lack of recent experience as a fabless semiconductor company making and selling proprietary ICs;
- level of intellectual property protection provided by our patents, the expenses and other consequences of litigation, including intellectual property infringement litigation, to which we may be or may become a party from time to time;
- vigor and growth of markets served by our licensees and customers and operations of the Company; and

other risks identified in MoSys' most recent report on form 10-K filed with the Securities and Exchange Commission, as well as other reports that MoSys files from time to time with the Securities and Exchange Commission. MoSys undertakes no obligation to update publicly any forward-looking statement for any reason, except as required by law, even as new information becomes available or other events occur in the future.

MoSys (Nasdaq: MOSY) Overview



Built on Differentiated IP ...

Expanding into a World-class Fabless Semiconductor Company

Phase 1
1991-1999

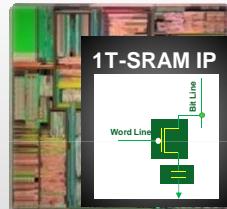
SRAM ICs
based on
1T-SRAM®



- 1991: MoSys founded by IDT
- Semiconductor company

Phase 2
1999-2009

Memory IP
for SoCs
1T-SRAM®



- 1999: New IP Business Model
- 2001: IPO

New Vision

2008:

Len Perham re-joins as CEO

- ❖ New team
- ❖ New vision

2009:

MoSys Acquires Prism Circuits

- ❖ High-Speed Interface IP

The New MoSys...2010-2025+

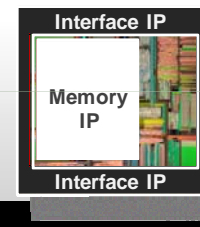
Differentiated ICs



Bandwidth Engine™

Networking...
Storage
Computing

Differentiated IP



IP for SoCs

Strong Foundation

- Differentiated, patented memory technologies & world-class high-speed interface IP
- Experienced leadership team
- Strong financial foundation: **~\$34M+ in cash & no debt**

Experienced Team



Len Perham
CEO

40+ Yrs Experience

- **CEO of IDT, Incubated MoSys**
...QED, Galileo, Centaur...
- **Chairman NetLogic Microsystems**
- **Zilog, AMD, Western Digital**

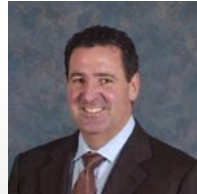
Sundari Mitra
EVP
Engineering



23+ Yrs Experience

- **Founder & CEO** of Prism Circuits
- Led 300+ SPARC team at **Sun**
- **Intel** engineering

Dave DeMaria
VP, Business
Operations



27+ Yrs Experience

- **CEO** Optimal
- Executive VP, **Cadence**
- **Successful** Business Leader

Michael Miller
VP Systems
Applications



30+ Yrs Experience

- **CTO of IDT**
- Over **25 Patents**, including JTAG
- Architected **QDR, ZBT, TCAMs**

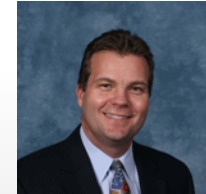
John Chin
Sr. Director
Foundries



25+ Yrs Experience

- **President of** Photonics
- **TSMC** – US & Taiwan
- VP Cadence, LSI

Jim Sullivan
CFO



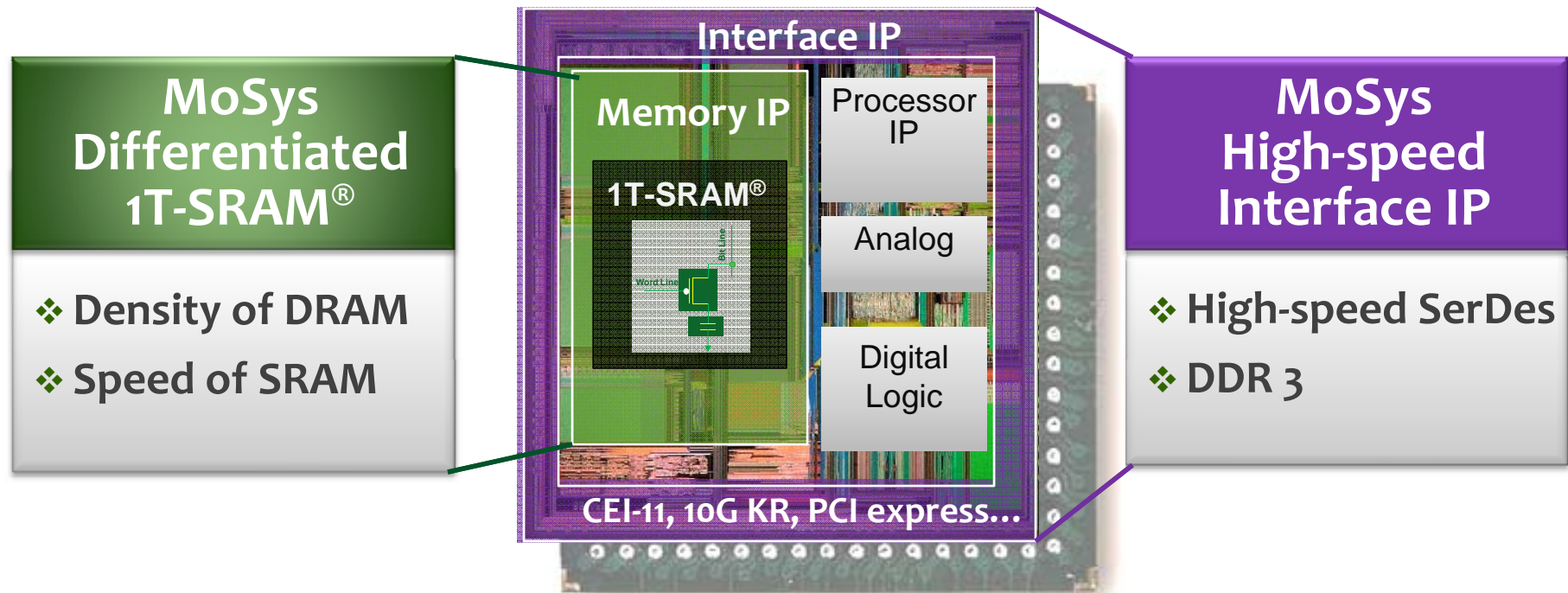
19+ Yrs Experience

- **CFO Apptera**
- **CFO 8x8**
- **CFO Netergy Microelectronics**

SoC Era...Growth in MoSys IP Business



World-class Provider of **Differentiated, High Value IP** for SoC Designers in Communications & Consumer



Types of IP

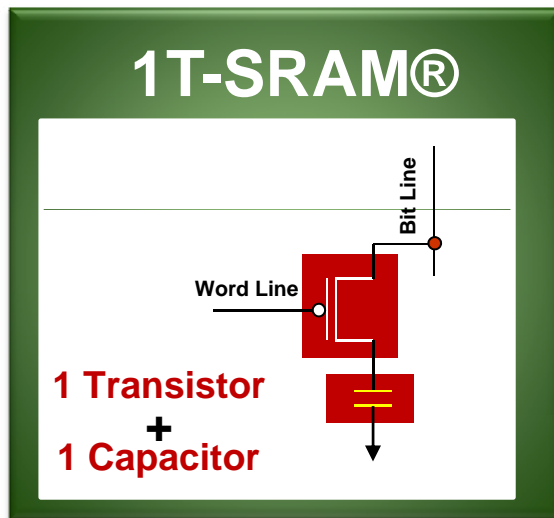
- \$ Non-differentiated: 6T-SRAM, standard I/Os
- \$\$ Design Services: Analog
- \$\$\$ **Differentiated:** ARM cores, **SerDes I/Os, 1T-SRAM**

Flagship 1T-SRAM[®] embedded Memory



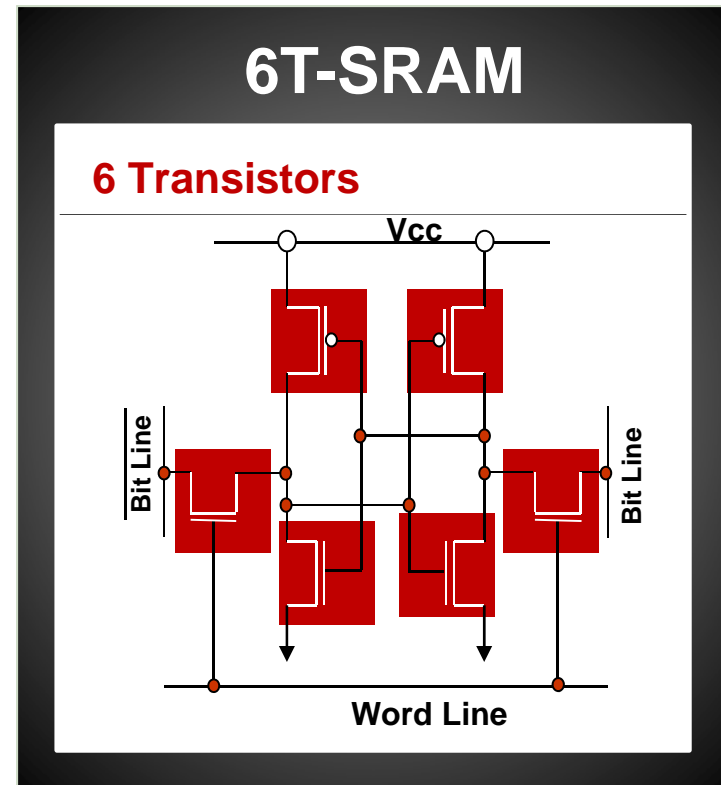
Patented 1T-SRAM[®] ...

The Density of DRAM, Speed of SRAM



1T-SRAM Advantages

- ❖ Up to 3X less silicon area = lower cost
- ❖ Lower power (1/2 the power of 6T)
- ❖ High-speed, low latency, random access
- ❖ Reliability advantages (SER – soft error rate)



More Than 225 Million Devices Shipped



Game Consoles, Handsets, Consumer Devices and Networking

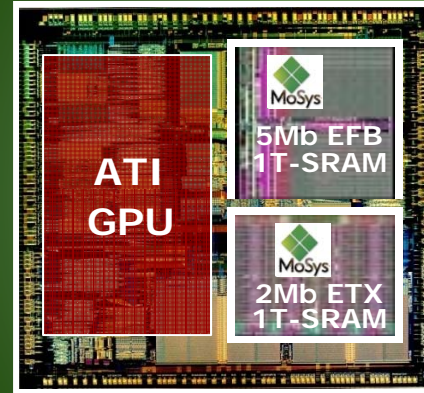
Nintendo
Wii



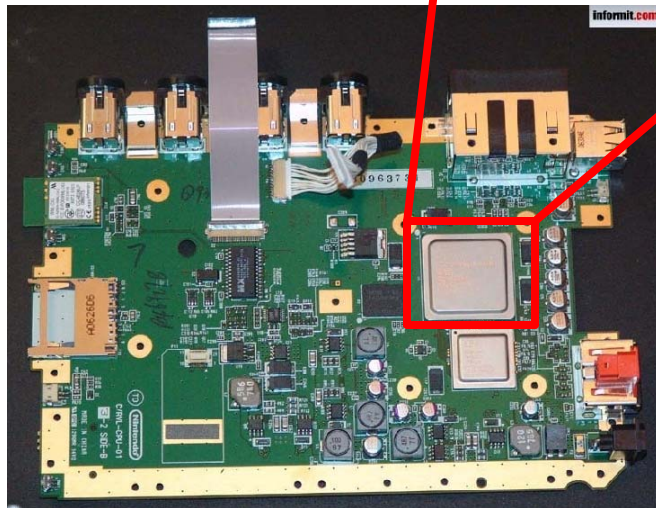
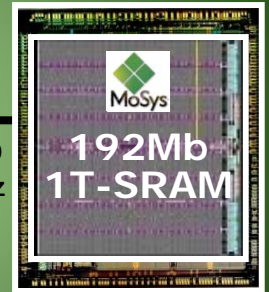
NEC
Graphics Processor



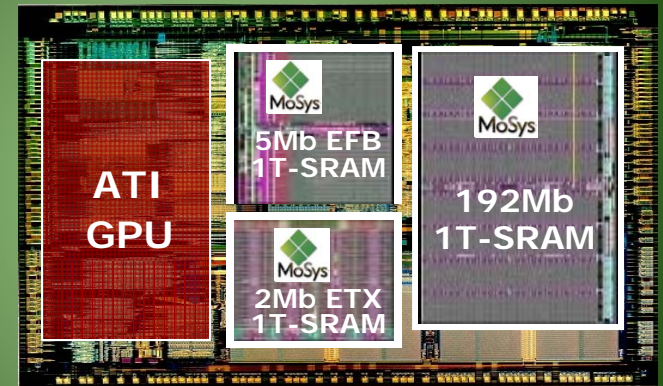
2 die
MCM



500
MHz



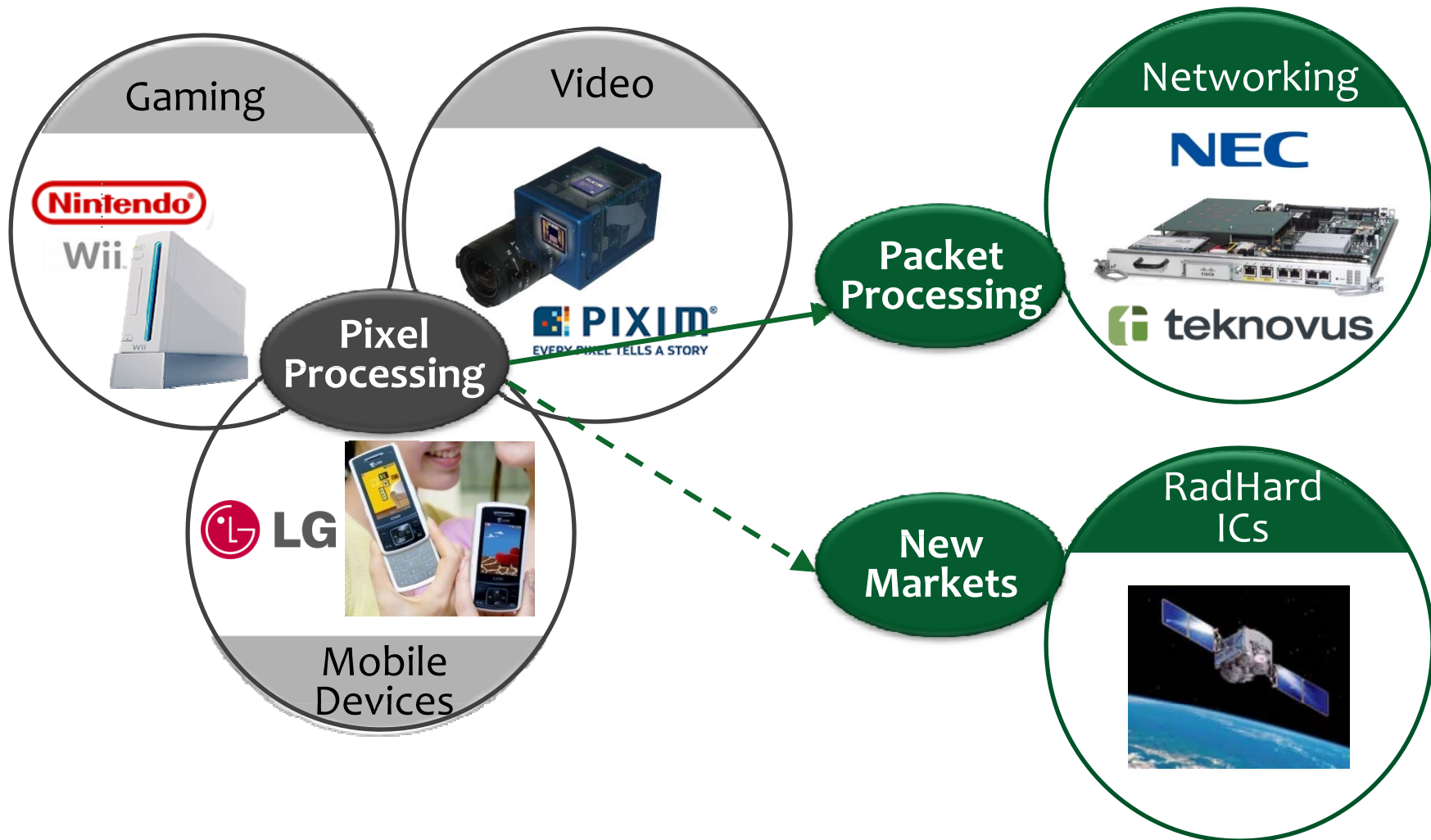
Current
Generation
Wii SoC



Key 1T-SRAM[®] Markets & Customers



Expanding SAM from Pixel Processing to New Markets



Multi-Gigabit, Multi-Protocol SerDes IP



Acquired Prism Circuits in June 2009 to Expand into \$300M Interface IP Market & add Team for Bandwidth Engine

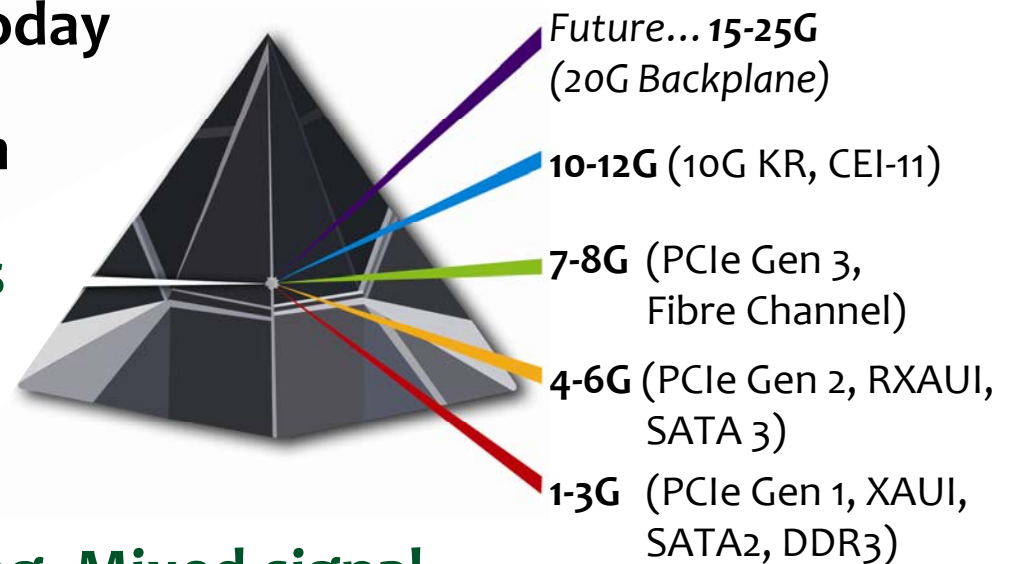
❖ **Multi-Gigabit:** 1-11 Gbps Today

❖ **Silicon-proven:** 65 & 40nm

❖ **MoSys SerDes Advantages**

- Lowest jitter
- Lowest power
- Smallest area

❖ **World-class Team of Analog, Mixed-signal and Systems Engineers**

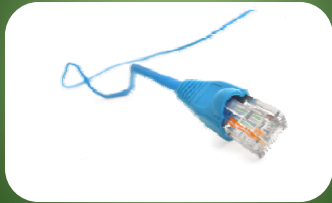


Key Target Markets for SerDes IP



Aligned with Target Markets for Bandwidth Engine Family of ICs

Networking



- CEI-11
- 10G KR
- PCIe
- XAUI/RXAUI
- SGMII
- DDR3

Storage



- SATA
- Fibre Channel
- PCI Express
- SAS
- DDR3

Computing



- PCI Express
 - Gen 3 (8 Gbps)
 - Gen 2 (5 Gbps)
 - Gen 1 (2.5 Gbps)
- DDR3

Consumer

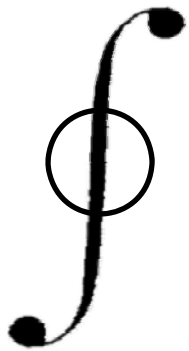


- PCI Express
 - Gen 3 (8 Gbps)
 - Gen 2 (5 Gbps)
 - Gen 1 (2.5 Gbps)
- DDR3

Introducing Bandwidth Engine™...



Breakthrough Solution for Networking Systems Breaking Bandwidth Barriers™



- ❖ Density of DRAM
- ❖ Speed of SRAM
- ❖ High reliability



- ❖ Removes I/O performance barrier
- ❖ Fewer pins & easier PCB routing



Bandwidth Engine™



The Big Picture...Entering the Zettabyte Era

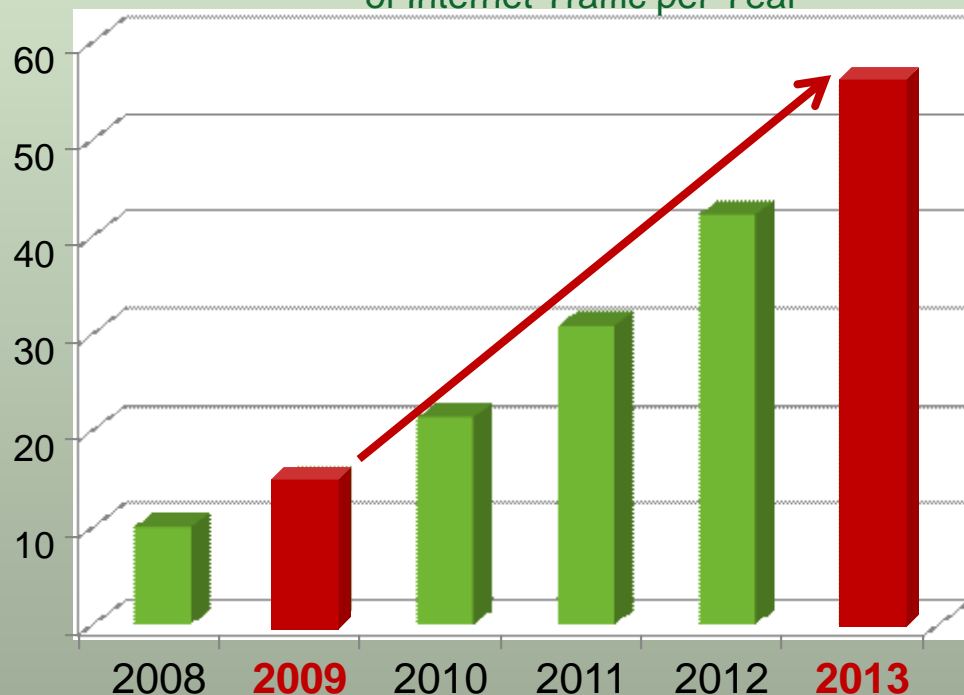


To Break the Bandwidth Barriers...

New Generation of Networking Systems Required

Internet Grows 4X by 2013

Approaching **1 Trillion Gigabytes**
of Internet Traffic per Year



Source: *Cisco Visual Networking Index*

❖ **New, Bandwidth Intensive Applications**

- Video-on-Demand, IPTV, P2P, Cloud Computing, VoIP...

❖ **Complex Packet Processing**

- QoS priority, IPv6, security, traffic policing...

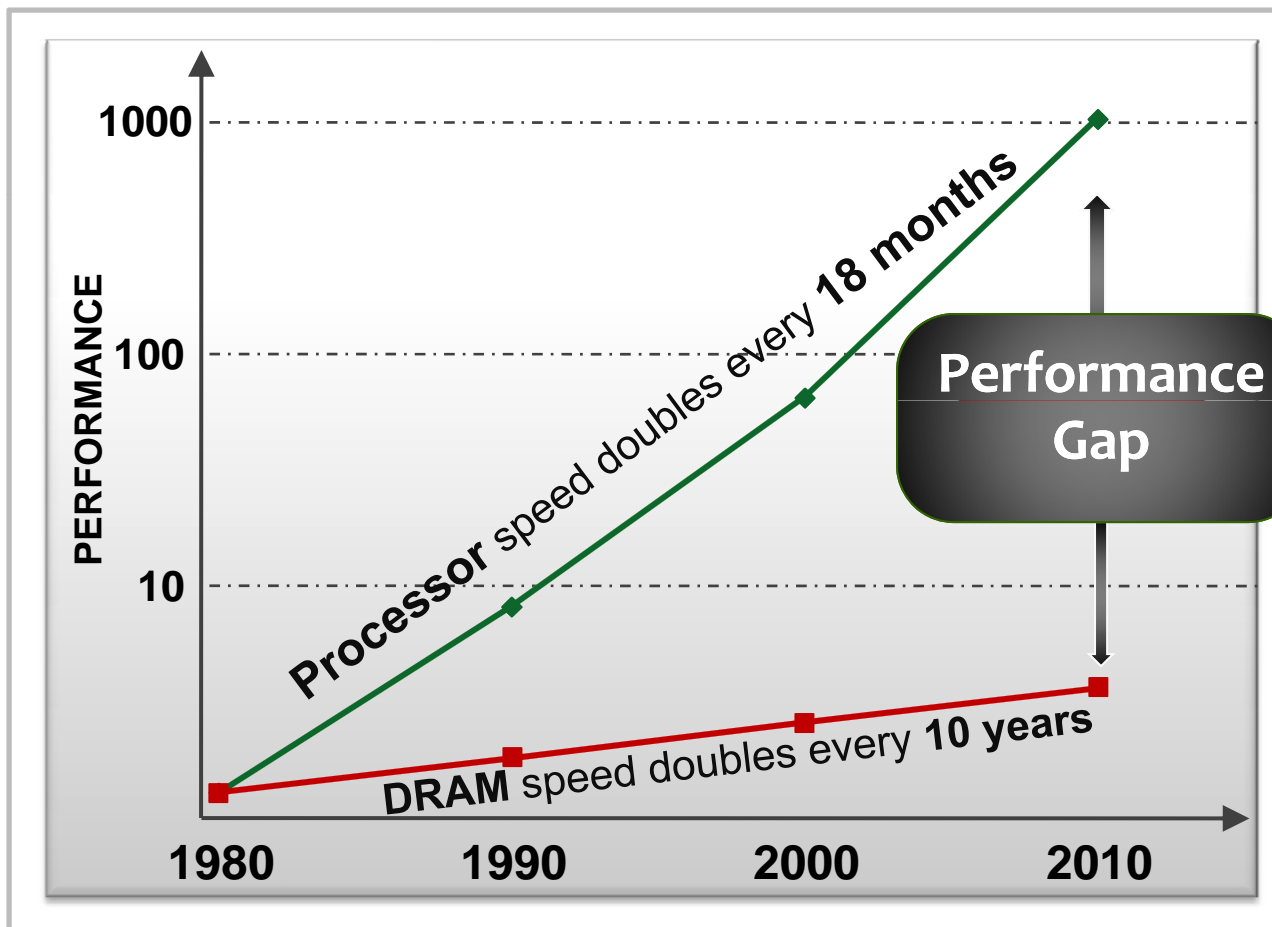
❖ **Service Providers Accelerating 40/100Gbe Adoption**

The System Performance Barrier



Memory Performance...

The Barrier to System Performance



Part of the Solution...

MoSys 1T-SRAM®

- ❖ Speed of SRAM
- ❖ Density of DRAM

Huawei CTO – April 27



“Multicore designers need to solve the growing **memory bandwidth problems** such chips face”

“We have thousands of independent operations going on **getting them to the memory & back is an organizational problem.**

There’s a cloud on the horizon for network processing because **as networks get denser we won’t be able to cope with the memory access needed”**

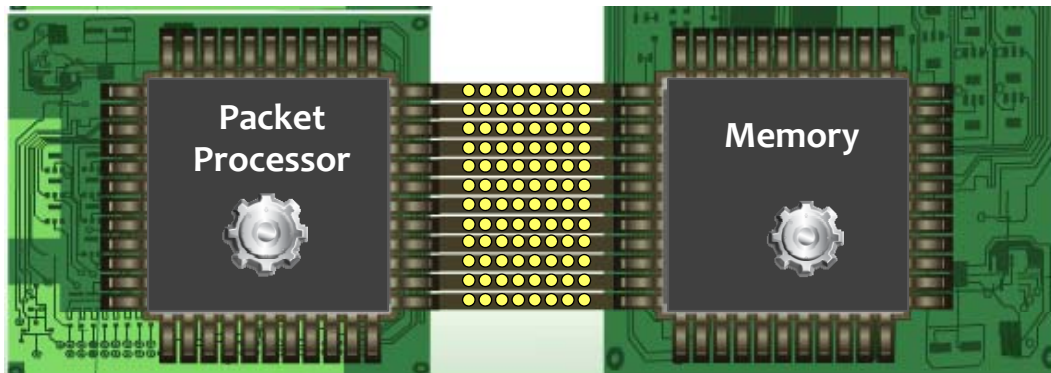
Alan Gatherer, CTO Baseband SoCs
Huawei

The Link...Another Barrier to System Performance

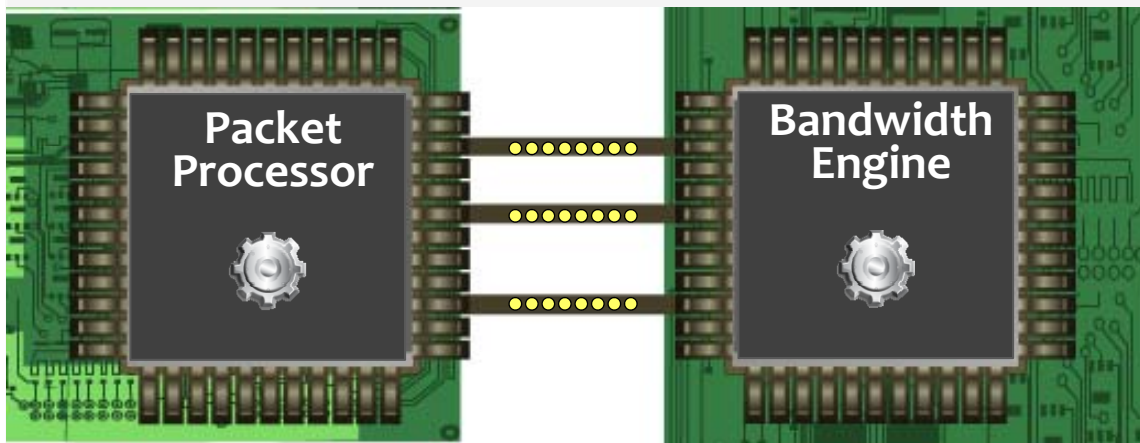


Today

... Parallel I/Os & a Memory



A New Solution... Bandwidth Engine



- ❖ Serial I/Os (SerDes)
- ❖ Efficient chip-chip transport (GigaChip™ Interface)
- ❖ Macro functions

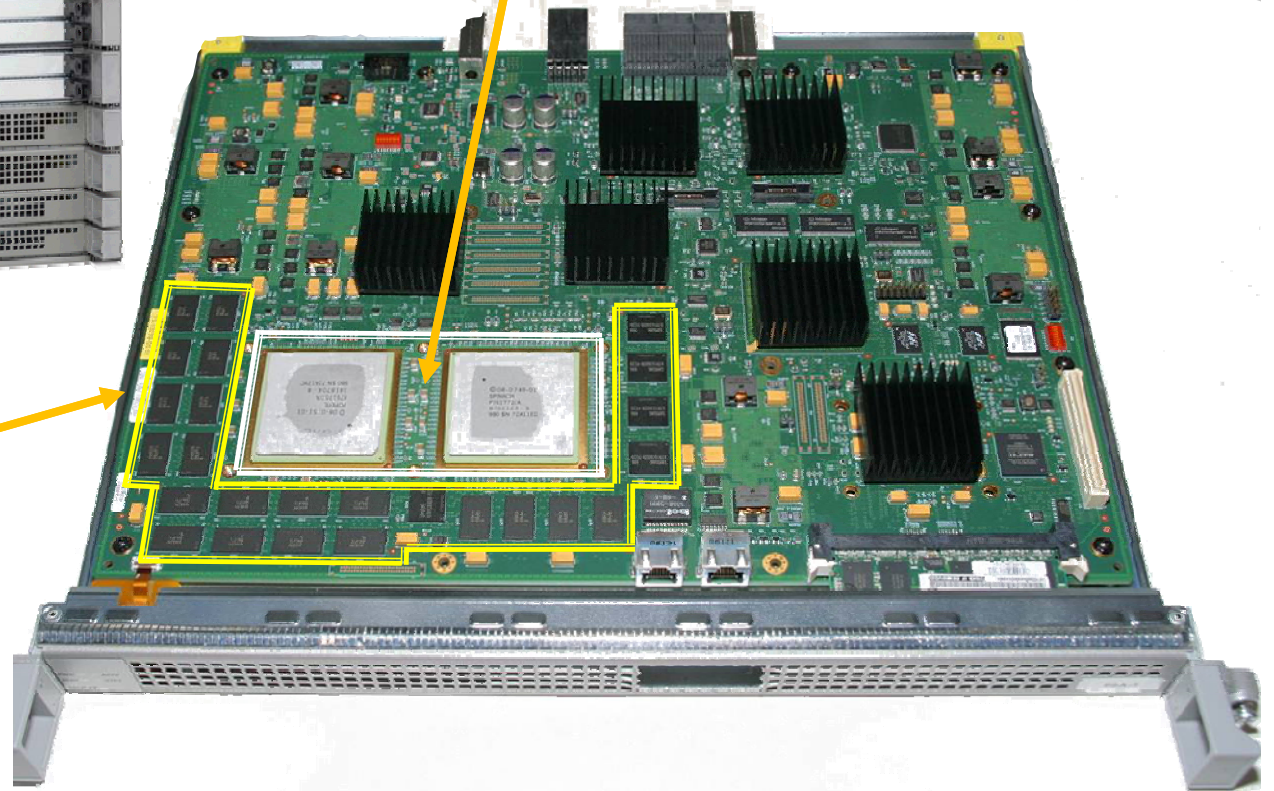
Today's 10/40G Networking Blades

The Memory Barrier...

Limiting Bandwidth Performance and Increasing System Cost



Packet Processing Engines



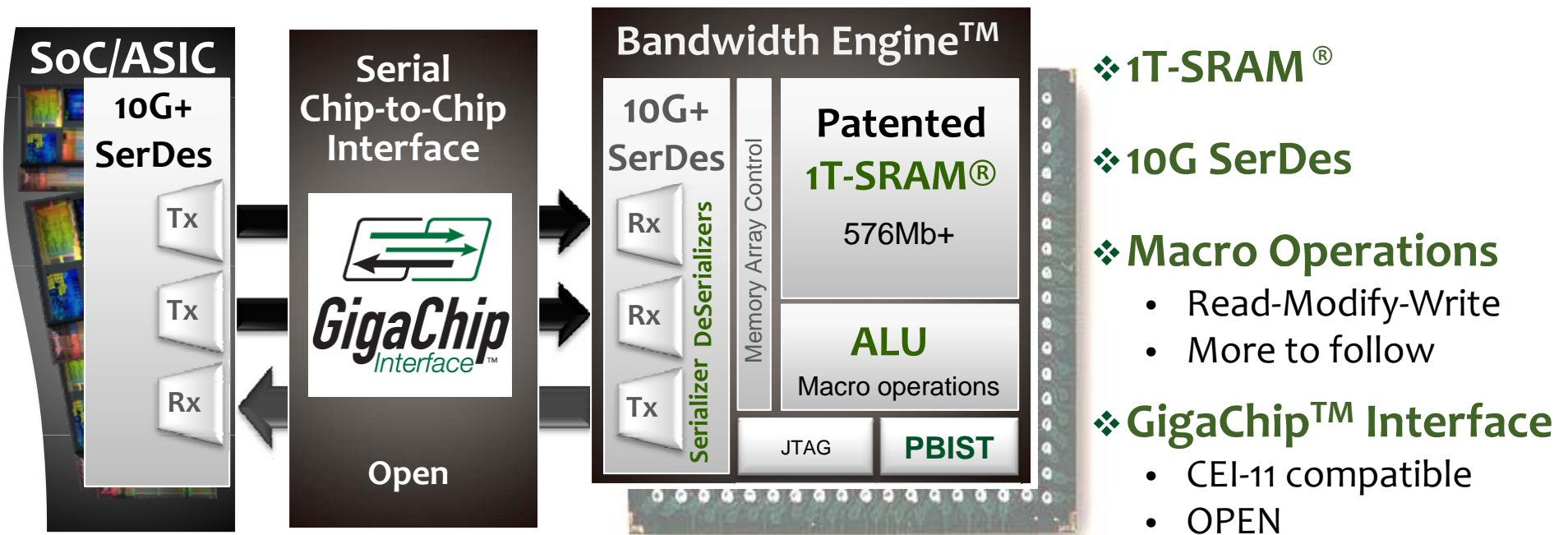
Sea of Memory

RLDRAM[®]

QDR SRAM

DDR 3

The Bandwidth Engine™ Solution



Unparalleled Performance: 2 GigaAccesses/sec

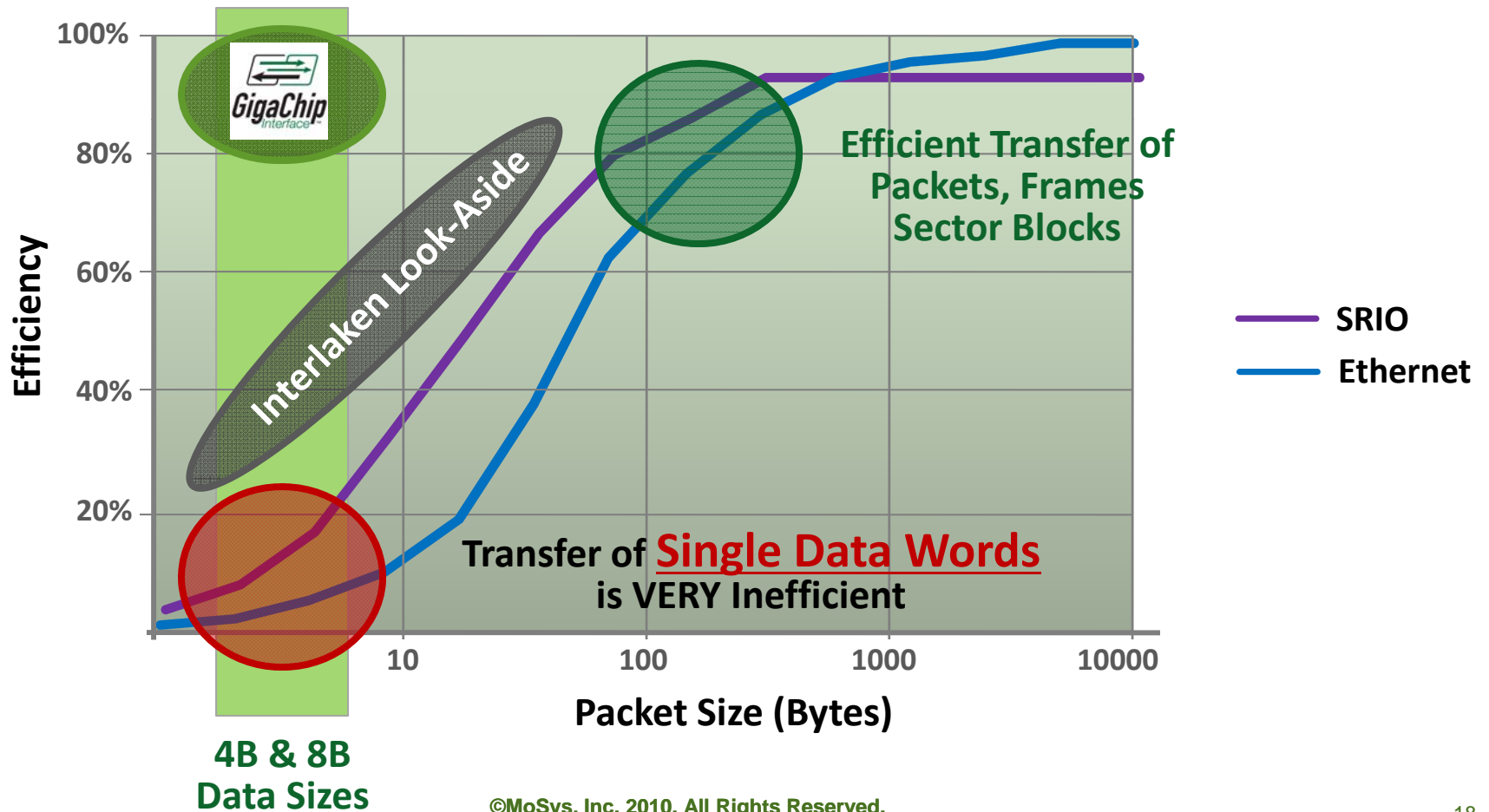
... up to 50% lower system cost, 40% lower power

Enlarging the Bandwidth of Serial Pipes



GigaChip™ Interface...

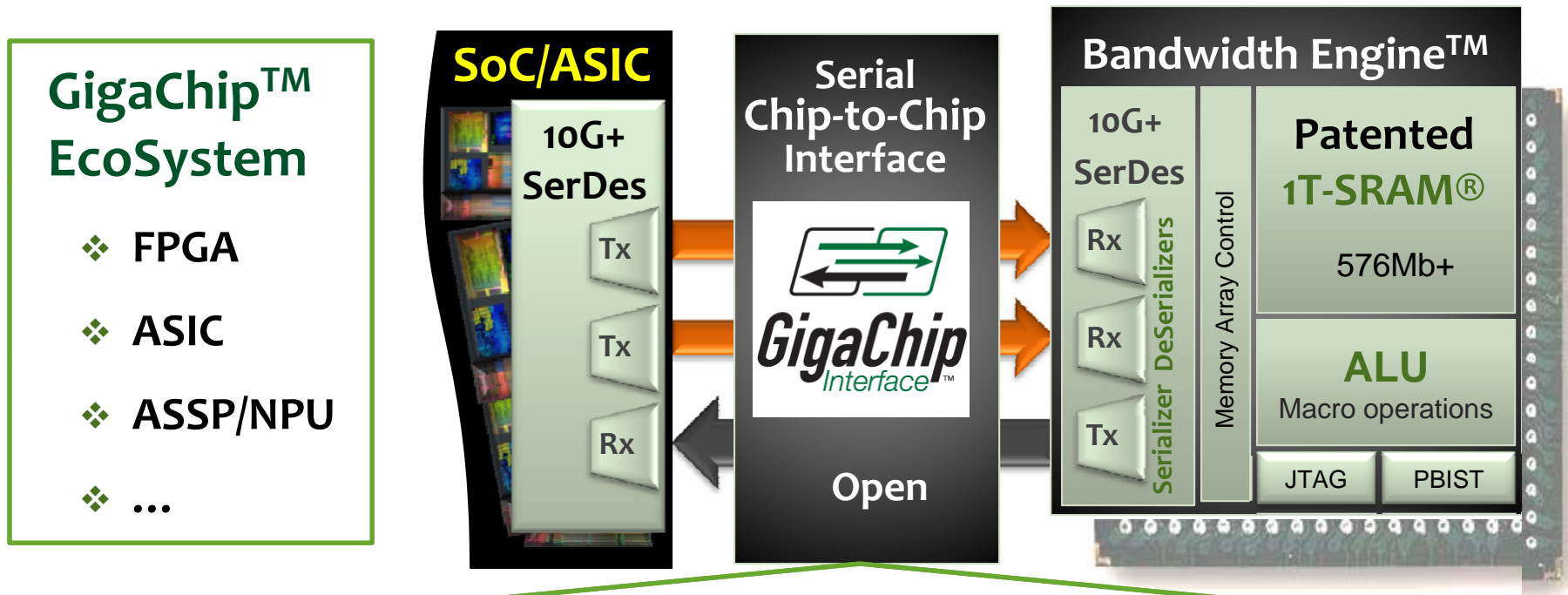
High Efficiency Transferring Single Data Words Chip-to-Chip



Breaking the Bandwidth Barriers

GigaChip™ Interface...

Enabling Next Generation Chip-to-Chip Communications

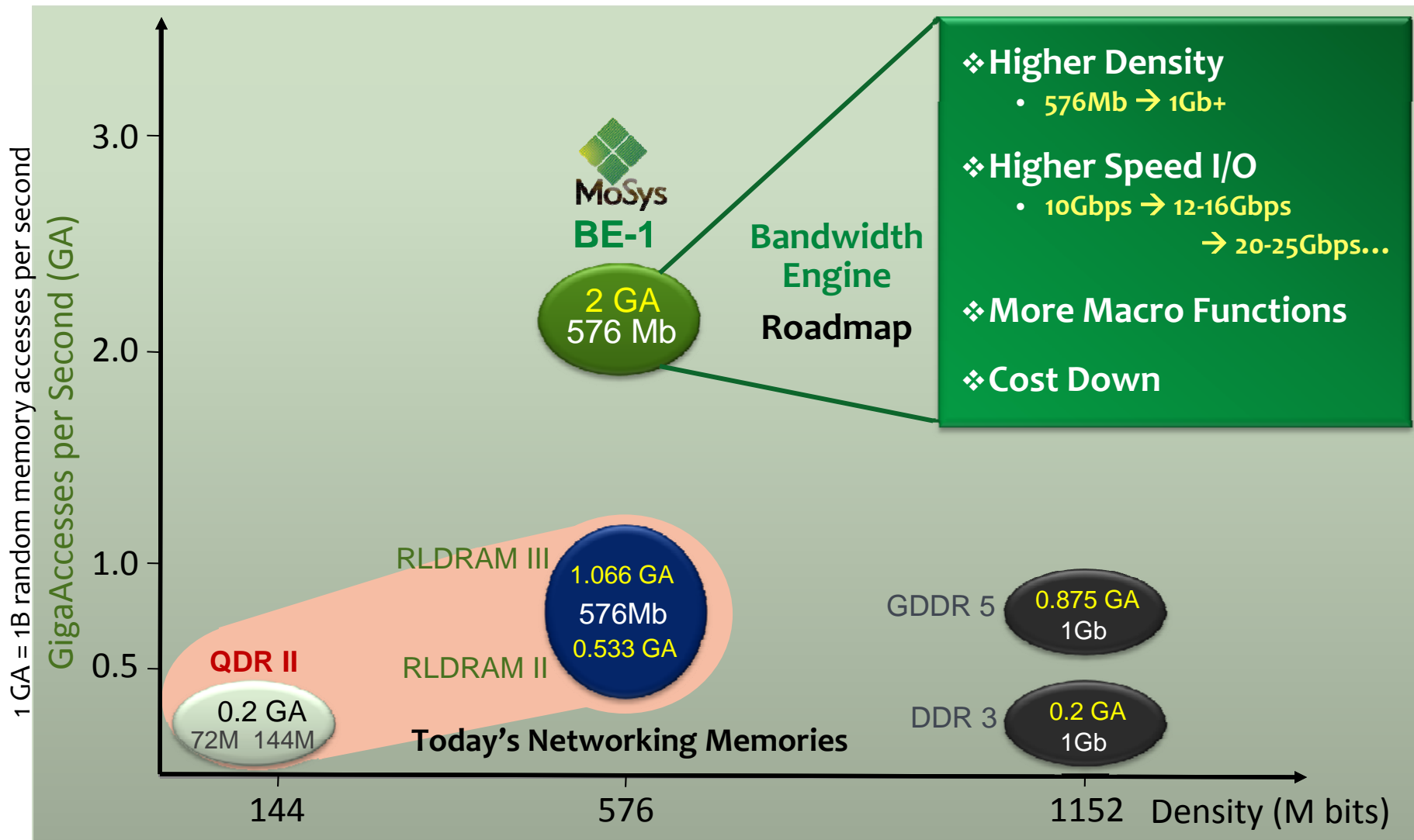


- ❖ OIF CEI-11 Compatible
- ❖ High Reliability
- ❖ High bandwidth density
- ❖ Light weight
- ❖ Highly Scalable
- ❖ High Efficiency (90%)

Beyond the Competition



Bandwidth Engine... the 1st solution breaking the 1 GA barrier



The Opportunity - \$1B+ TAM



Bandwidth Engine Family Dramatically Expands MoSys Market Opportunity

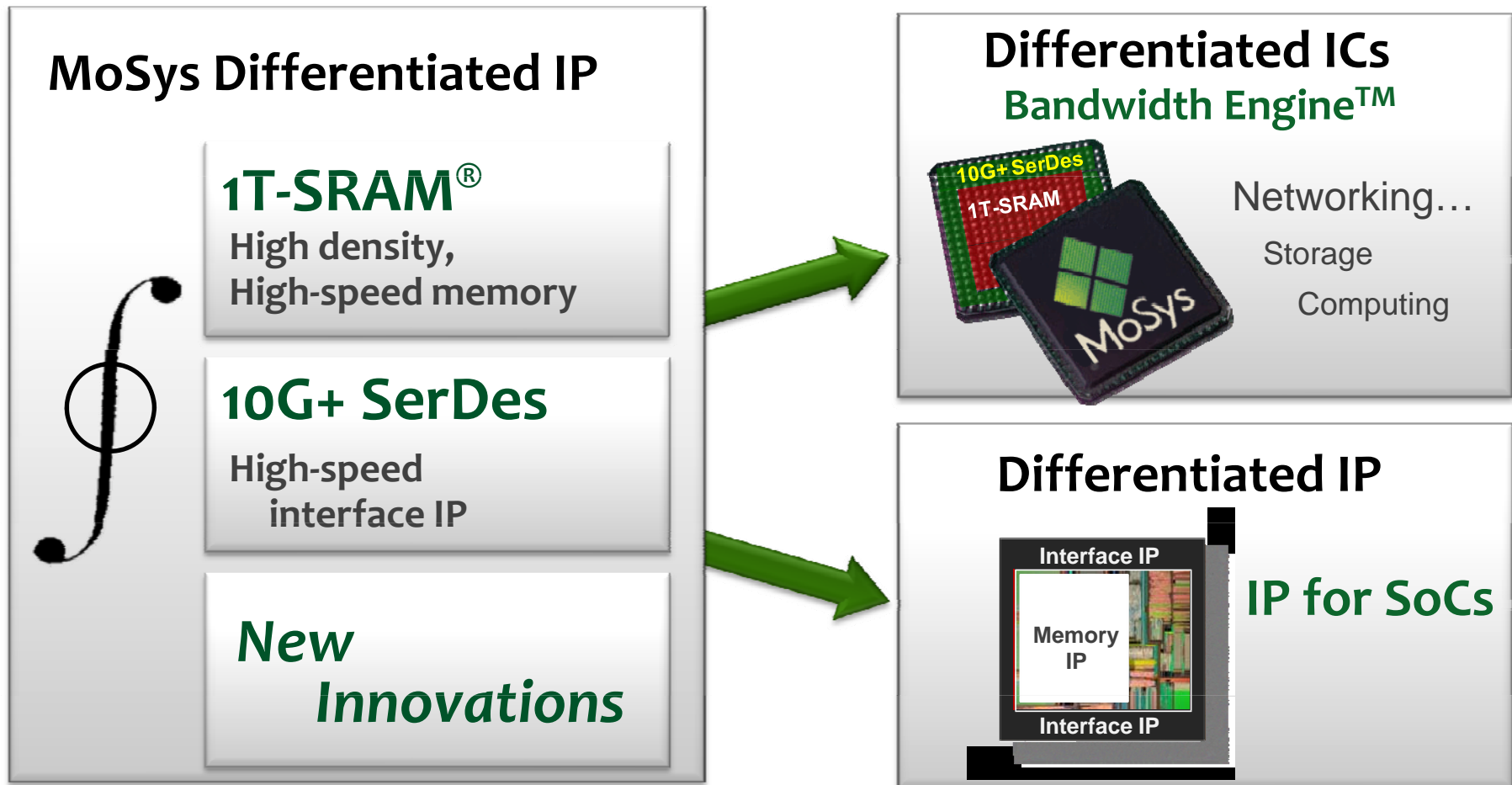
- ❖ **Initial Target is Networking Applications - \$700M-\$1B+**
- ❖ **Opportunities in Storage & Server Applications**
- ❖ **Bandwidth Engine is our First in a Family of IC Products
... More to Come**

The New MoSys Business Model



Fabless Semiconductor Company

... Built on Differentiated IP





Financial Overview

Jim Sullivan - CFO

MoSys Business & Financial Update



❖ Revenue of \$3.6M in 1Q10

- **Licensing revenue of \$1.6M, up 18% sequentially:**
 - Highest quarterly licensing revenue since 2Q07;
 - Added 8 new licensees in 2009;
 - Developing off-the-shelf IO products to expand markets;
 - Expect strong revenue ramp in 2010 from backlog at 3/31;
- **Royalty Revenue of \$2.0M**
 - NEC (Wii & other) & TSMC largest royalty

❖ Cash - \$33.9M at 3/31

- \$6.5M burn in Q1 includes \$2.3M for acquisition & R&D investment in Bandwidth Engine & 1T-Flash

❖ Acquired MagnaLynx, Inc.

- Added low-power Serdes IP, technology & expertise;
- \$5M cash purchase - \$3.5M paid in 2H09 & \$1.0M earn-out & \$0.5 holdback payable in 2011

MoSys IP Revenue Model



Customer Design Win

- Customize IP Macro (if not off-the-shelf)
- Customer SoC development and IP integration

Customer ships SoC to Market

❖ License Revenue

- NRE – if customized

❖ License Fees

- Revenue from 13 licensees in Q110
- Revenue from newly acquired interface products

❖ Royalty revenue (memory)

- 100% margin
- Revenue from 14 licensees in Q110

- ❖ **Gross Margins – 80% and higher**
- ❖ **Revenue recognized on % of completion basis**
- ❖ **Acceptance clauses can delay revenue recognition**

Summary Income Statement (Non-GAAP*)



In Millions, except EPS

	Q1 10	2009	Q4 09	Q3 09	Q2 09	Q1 09
Total Revenues	\$ 3.6	\$ 11.5	\$ 3.5	\$ 3.4	\$ 2.0	\$ 2.6
Licensing	1.6	3.4	1.3	1.3	0.3	0.5
Royalty	2.0	8.0	2.2	2.1	1.7	2.0
Gross Profit	2.9	9.7	2.9	2.8	1.7	2.3
<i>Margin %</i>	<i>80%</i>	<i>85%</i>	<i>82%</i>	<i>82%</i>	<i>88%</i>	<i>89%</i>
R&D	4.5	15.5	4.2	4.2	3.5	3.6
SG&A	2.1	7.6	2.0	1.7	1.7	2.2
Total Op Ex	6.6	23.1	6.2	5.9	5.2	5.8
Net Loss	\$ (3.7)	\$ (12.5)	\$ (2.8)	\$ (3.0)	\$ (3.3)	\$ (3.4)
EPS	\$ (0.12)	\$ (0.40)	\$ (0.09)	\$ (0.10)	\$ (0.11)	\$ (0.11)

*Non-GAAP:

- ❖ Excludes non-cash charges: stock-based compensation and amortization of acquired intangible assets
- ❖ Excluded cash charges: acquisition-related contingent compensation expense, acquisition transaction costs and restructuring charges

Strong Balance Sheet



As of March 31, 2010

(In millions except headcount)

▣	Cash and Investments	\$33.9
▣	Total Assets	73.5
▣	Debt	0.0
▣	Liabilities	13.7
▣	Stockholders' Equity	\$59.8
▣	Total Shares Outstanding	31.3
▣	Headcount (80%+ is Engineering)	164

MoSys – Reasons to Invest



Built on Differentiated IP ...
Expanding into a World-class Fabless Semiconductor Company

Differentiated ICs Bandwidth Engine™



Networking...
Storage
Computing

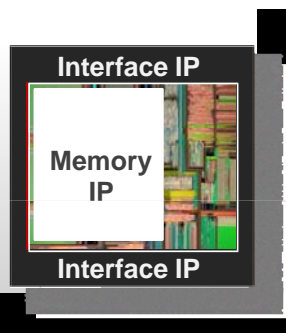
Bandwidth Engine™ IC Family ... Big Long-term Growth Opportunity

- ❖ \$1B+ Market Opportunity
- ❖ Revenue Starts to Ramp in 2011

Memory & Interface IP ... Good Short-term Growth Opportunity

- ❖ Market Expansion for 1T-SRAM
- ❖ SerDes \$300M opportunity

Differentiated IP



IP for SoCs

Strong Foundation

- ❖ Proven Leadership Team
- ❖ Differentiated, Patented IP Portfolio
- ❖ \$34M+ Cash, no debt

Thank You!



Contacts

Jim Sullivan, MoSys, Inc. CFO

408-731-1846

jsullivan@mosys.com

Beverly Twing, Shelton Investor Relations

972-239-5119 x126

btwing@sheltongroup.com