

UNITED STATES COURT OF APPEALS FOR THE FEDERAL CIRCUIT

717 MADISON PLACE, N.W.

WASHINGTON, D.C. 20439

JAN HORBALY
CLERK

TELEPHONE: 633-6550
AREA CODE 202

December 12, 2002
BY FACSIMILE:

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Re: Rambus Inc. v. Infineon Technologies, 01-1449

Dear Counsel:

This supplements the court's request for documents in the above-noted appeal.

If the entire JEDEC SDRAM standard is part of the trial record, provide the court with the entire SDRAM standard and identify the relevant page numbers regarding 2-bank SDRAM designs, externally supplied reference voltage, phase lock loops, programmable CAS latency, and programmable burst length.

We would appreciate that you also expedite this request. As before, if agreed upon by both parties, please provide only one copy of the requested documents.

If you have any questions regarding this request, please contact Supervisory Deputy Clerk Linda Purdie or Senior Deputy Clerk Karen Smagala Hendrick at 202-312-5523.

Sincerely,



Jan Horbaly
Clerk