



ACTEL CORPORATE FACT SHEET

August 2006

THE COMPANY

Actel Corporation is the leader in single-chip FPGA solutions. The Company is traded on the NASDAQ National Market under the symbol ACTL and is headquartered at 2061 Stierlin Court, Mountain View, Calif., 94043-4655. For more information, visit <http://www.actel.com>. Telephone: 888-99-ACTEL (992-2835).

FINANCIAL INFORMATION

- First product shipped in 1988
- Added to the S&P Small Cap 600 in 1999
- Fiscal year 2005 revenues: \$179 million
- Strong balance sheet: \$186 million cash, no debt
- More than 570 employees worldwide
- #1 nonvolatile FPGA supplier

MANAGEMENT

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| • John East | chief executive officer and president |
| • Esmat Hamdy | senior vice president, technology and operations |
| • Dennis Kish | senior vice president, sales and marketing |
| • Fares Mubarak | senior vice president, engineering |
| • Jon Anderson | vice president, finance and chief financial officer |
| • Jim Davis | vice president, software engineering |
| • Esam Elashmawi | vice president, silicon engineering |
| • Tony Farinaro | vice president and general manager, design services |
| • Barbara McArthur | vice president, human resources |
| • David Van De Hey | vice president and general counsel |

SALES AND DISTRIBUTION

Actel markets its products through a worldwide direct sales and distribution network. The company's North American sales network includes distributor Avnet Memec. Supplemented by offices in France, Germany, Italy and Sweden, Actel's European headquarters in the United Kingdom manages distributors throughout Europe and the Middle East. Actel's offices in Japan, Korea and China manage distributors throughout the Asia-Pacific region.

PRODUCT HIGHLIGHTS

Actel Fusion™ Programmable System Chip

- World's first mixed-signal FPGA, incorporating analog functions, embedded flash memory and FPGA fabric in single chip
- Density: 90,000 to 1,500,000 system gates

Actel IGLOO™ FPGAs

- The low-power, high-density reprogrammable ASIC alternative for portable applications
- Lowest power FPGA at 5μW and only low-power FPGA to support 1.2V
- Quick and easy power control with flexible implementation options, including the Flash*Freeze™ mode
- Density: 30,000 to 3,000,000 system gates

ProASIC3/E FPGAs

- World's lowest cost FPGA solution offering a breakthrough in price, performance, density and features
- Third generation of single-chip nonvolatile, reprogrammable, flash-based FPGAs
- Density: 30,000 to 3,000,000 system gates

RTAX-S FPGAs

- Space-optimized, radiation-tolerant devices with inherent nonvolatile benefits of single chip and high reliability enable Actel to directly compete for ASIC sockets
- Density: 250,000 to 4,000,000 system gates

CoreMP7 Soft ARM7 Processor Core

- Industry's only soft 32-bit ARM7 microprocessor core for FPGAs
- Optimized for integration in ARM-enabled ProASIC3 and Fusion devices

Software Solutions

The Actel Libero™ integrated design environment (IDE) seamlessly integrates best-in-class design tools from leading EDA vendors, such as Magma Design Automation, Mentor Graphics, SynaptiCAD and Synplicity, with Actel-developed custom tools into a single FPGA development package. The Libero IDE also includes Actel's Designer physical design solution. Usable with the Actel Libero IDE, the company offers the CoreConsole IP Deployment Platform (IDP) to enable designers to quickly stitch intellectual property (IP) blocks together into synthesizable and simulatable RTL. The company also offers Silicon Explorer, its design verification and debug tool.

Intellectual Property (IP) Cores

Designed, optimized and verified to work with Actel's FPGAs, the company offers more than 130 proven IP cores for consumer, automotive, embedded, high-performance communications, networking, telecom, high-reliability and military applications. When implemented in Actel devices, these cores offer designers the ability to streamline design and the benefits of faster time to market, reduced design costs and risks.

Flash FPGA Solutions

Actel provides its customers with a range of single-chip, flash-based solutions to address design challenges in the avionics, communications, consumer and industrial markets. With densities ranging from 75,000 to 3,000,000 system gates, the company's reprogrammable FPGA families offer the inherent benefits of Actel's nonvolatile flash FPGA technology – low power, live at power up, security and neutron immunity.

Antifuse FPGA Solutions

Actel offers a broad portfolio of nonvolatile antifuse-based FPGAs to solve the performance, power, security and reliability needs for the aerospace, automotive, avionics, communications, consumer, industrial and military markets. Ranging in density from 3,000 to 4,000,000 system gates, the company's single-chip solutions include FPGAs qualified to commercial, industrial, military and automotive specifications as well as radiation-tolerant and radiation-hardened devices.

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