



MoSys Corporate Overview

Chet Silvestri, President & CEO



Safe Harbor Statement

This presentation may contain forward-looking statements about the Company including, without limitation, benefits and performance expected from use of the Company's 1T-SRAM® technologies. Forward-looking statements are based on certain assumptions and expectations of future events that are subject to risks and uncertainties. Actual results and trends may differ materially from historical results or those projected in any such forward-looking statements depending on a variety of factors. These factors include but are not limited to, customer acceptance of our 1T-SRAM® technologies, the timing and nature of customer requests for our services under existing license agreements, the level of commercial success of licensees' products, ease of manufacturing and yields of devices incorporating our 1T-SRAM®, our ability to enhance the 1T-SRAM® technology or develop new technologies, the level of intellectual property protection provided by our patents, the vigor and growth of markets served by our licensees and customers, and operations of the Company and other risks identified in the Company's most recent annual report on Form 10-K filed with the Securities and Exchange Commission, as well as other reports that MoSys® files from time to time with the Securities and Exchange Commission. MoSys® undertakes no obligation to update publicly any forward-looking statement for any reason, except as required by law, even as new information becomes available or other events occur in the future.



MoSys Background

- **Company**
 - Founded in 1991; IPO in 2001 [MOSY]
 - Leadership: extensive semiconductor industry expertise
- **Rich technology foundation**
 - 500+ person years of research and development
 - 97 U.S. and 72 foreign patents & patents pending
- **Broad leader in SoC embedded memory IP**
 - 1T-SRAM® - Low Power and High Speed offerings
 - Highest density embedded memory available
 - Novel embedded FLASH product leverages technology
- **Extensively adopted and market proven**
 - Included in over 110,000,000 units shipped
 - > 175 contracts executed



MoSys Value Proposition

The MoSys 1T memory solutions are ideally suited to high volume low power consumer applications. They enable a completely new class of devices by reducing die area, active power and cost. MoSys has a unique and enviable record in consumer electronics with over 100 million shipped units

Nintendo's Ongoing MoSys Adoption

Gamecube



2001-2005

Wii



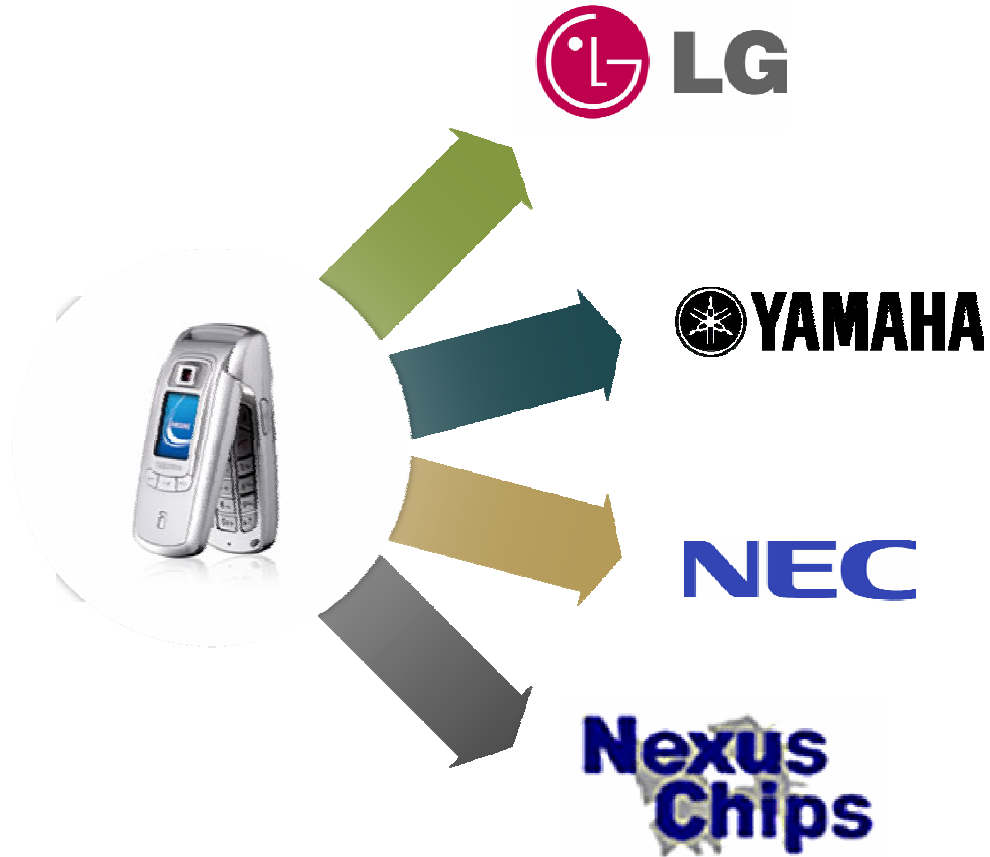
2006

"Designing the Wii console required an incredible list of breakthroughs in technology and innovation. The performance delivered by MoSys' 1T-SRAM technology is an important element of our solution," said Genyo Takeda, senior managing director, general manager, Integrated Research & Development Division at Nintendo. "The graphic performance of Wii benefits from MoSys' ability to develop highly innovative and dependable embedded memory products."

Current Design Wins: Graphics/Imaging



Current Design Wins: Cell Phones



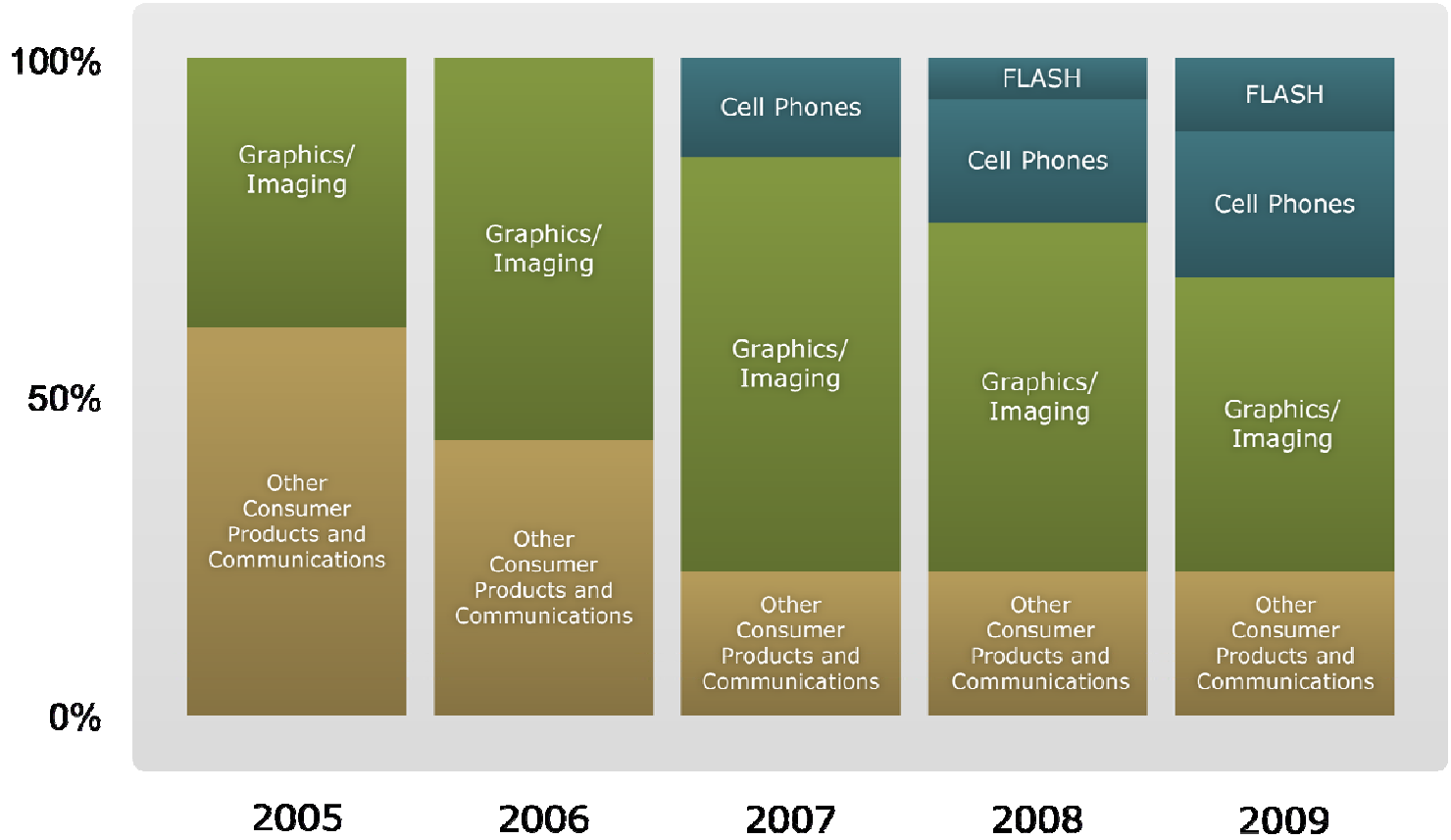
Efficient Technology Proliferation



"1T-SRAM is a perfect solution for the growing need for high-density embedded memories in our products," said Junji Torii, General Manager of Product Planning Department, Semiconductor Division at YAMAHA Corporation. Its optimum combination of density, speed and power dissipation allows YAMAHA to continue innovating our lineup of mobile audio LSIs, maintaining our leadership position in terms of performance, quality and cost."



MoSys Royalty Mix



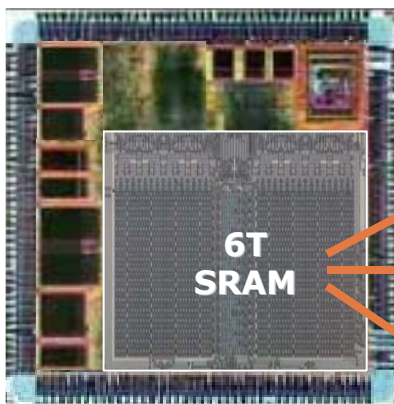
- **1T-SRAM CLASSIC Memory Macros**
 - Licensed to fabless semiconductor companies
 - Silicon proven down to 65nm
 - Low-Power and High-Speed optimized versions
 - Available at all major pure-play foundries

- **1T-SRAM Technology Licenses**
 - Available to IDM companies
 - Deployed on their in-house manufacturing processes
 - IDM creates & supports their specific configurations

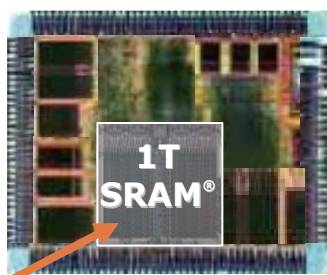
- **1T-FLASH (Introduction in '07)**
 - Novel approach on standard CMOS logic process
 - Solves longstanding problem for SoC developers
 - Business terms will be similar to 1T-SRAM

SoC Impact

Existing Production SoC

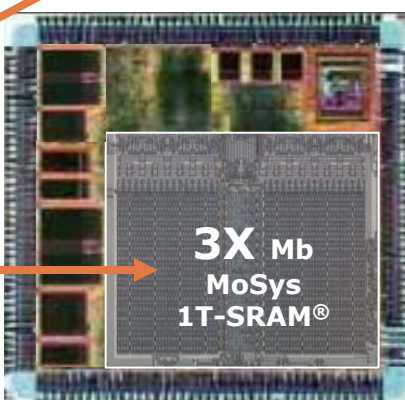


3:1 Area Reduction



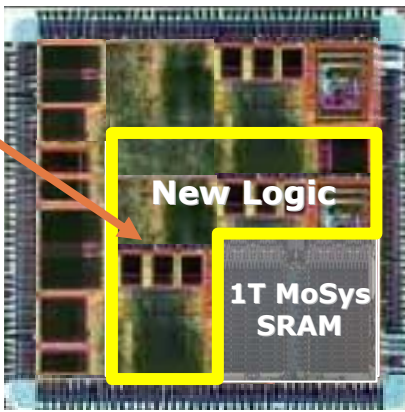
Higher Margins Or Lower ASP

Memory Increase



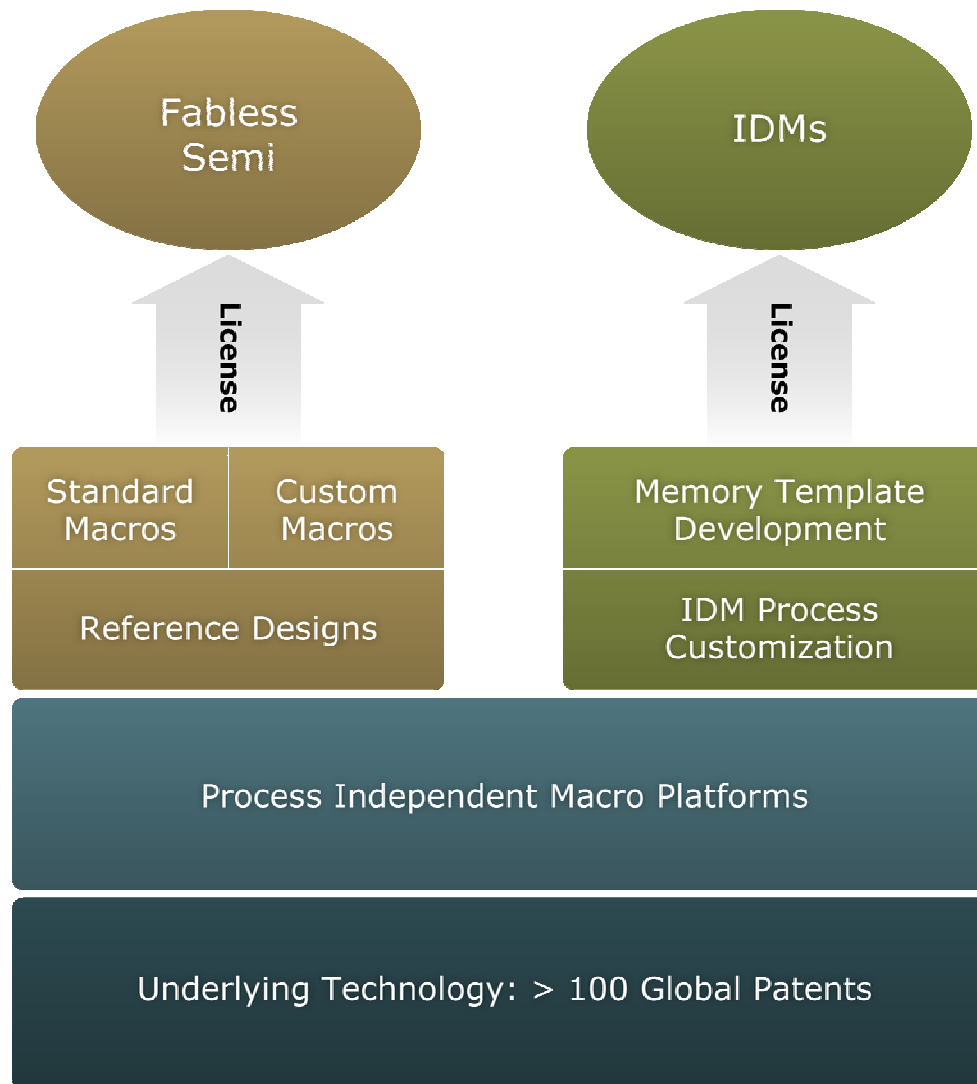
Higher Margins Or Higher ASP

Logic Density Increase



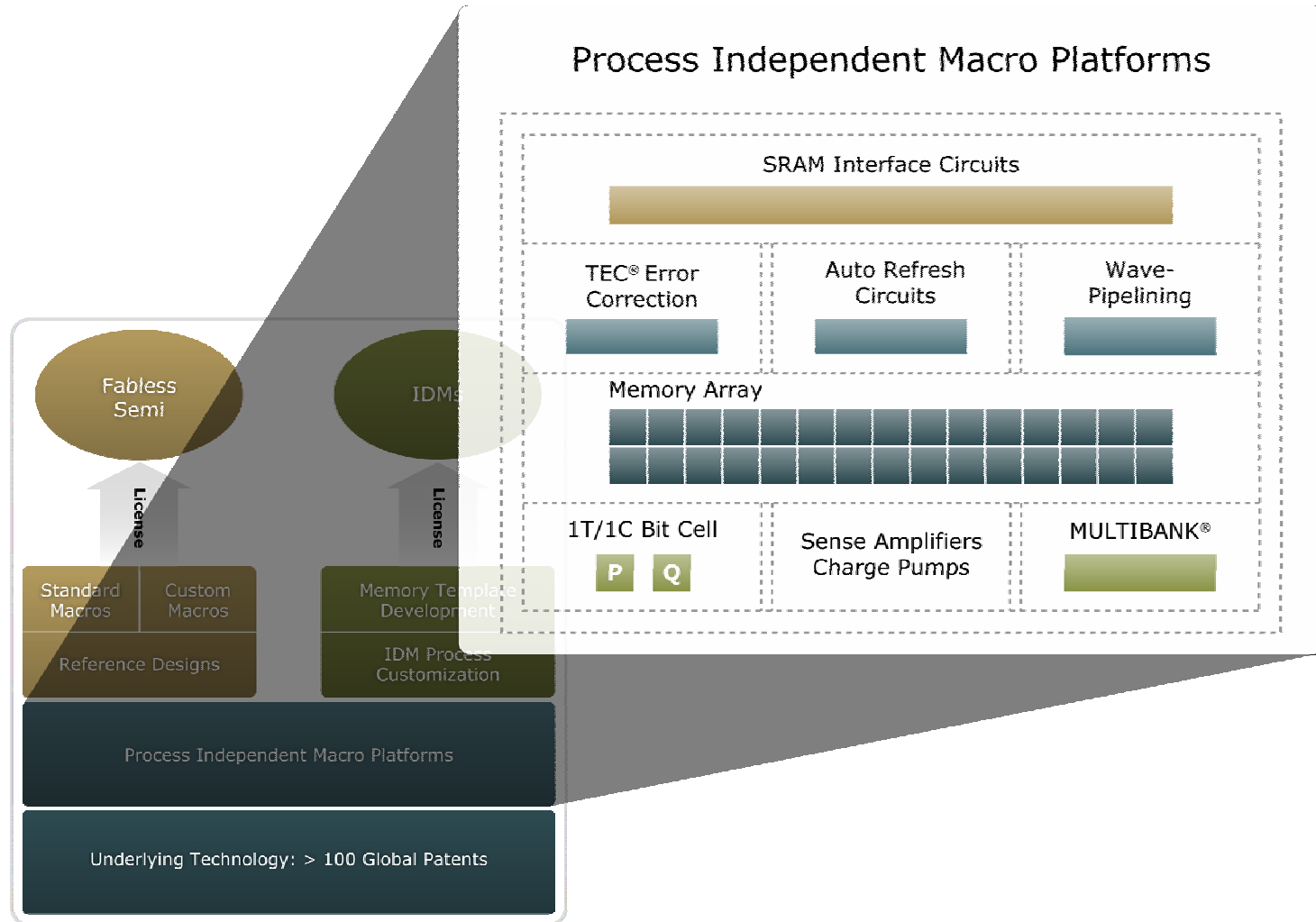
Higher Margins Or Higher ASP

Flexible Customer Delivery Model



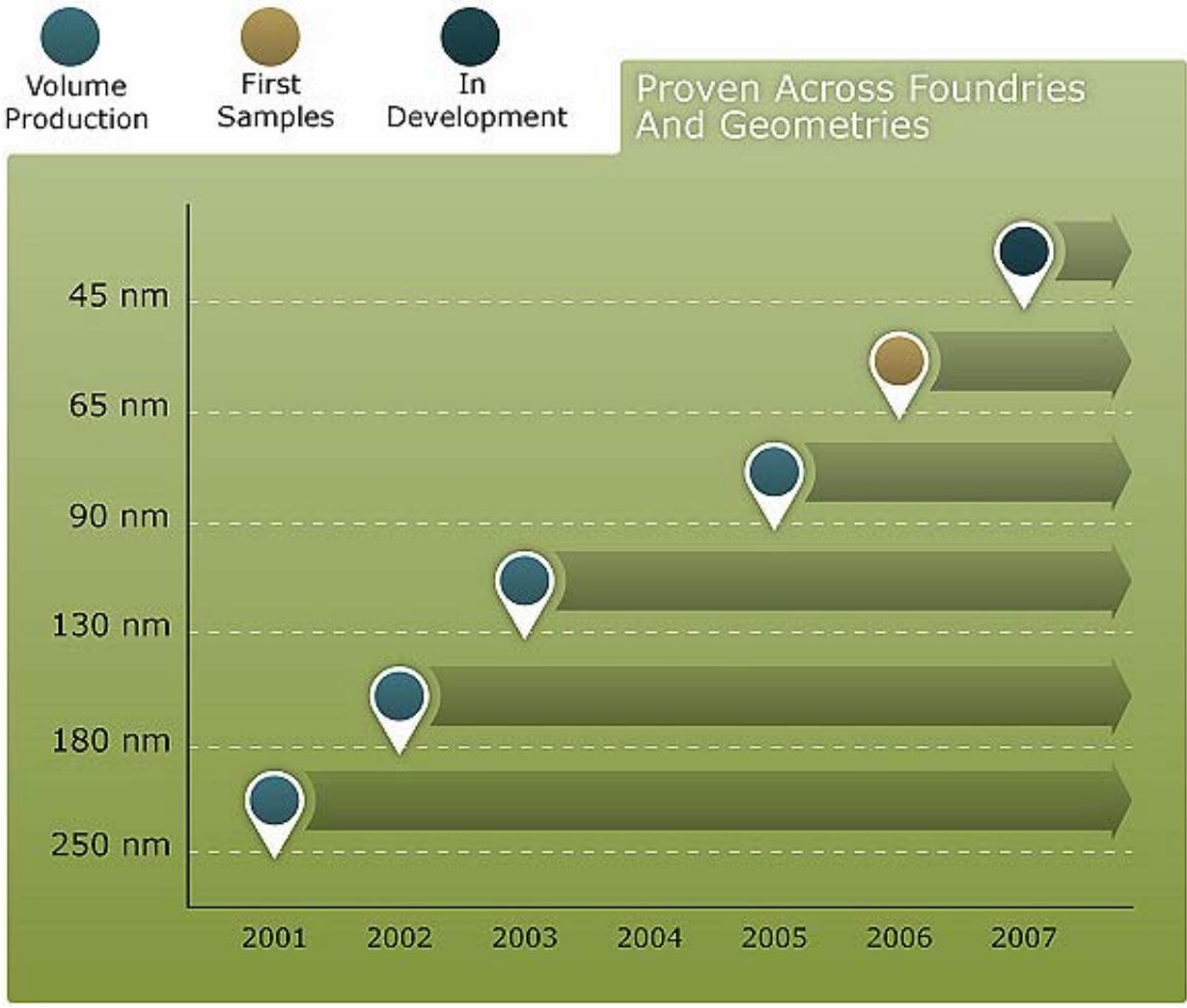


Our Technology Platform





Proven Scalability & Portability



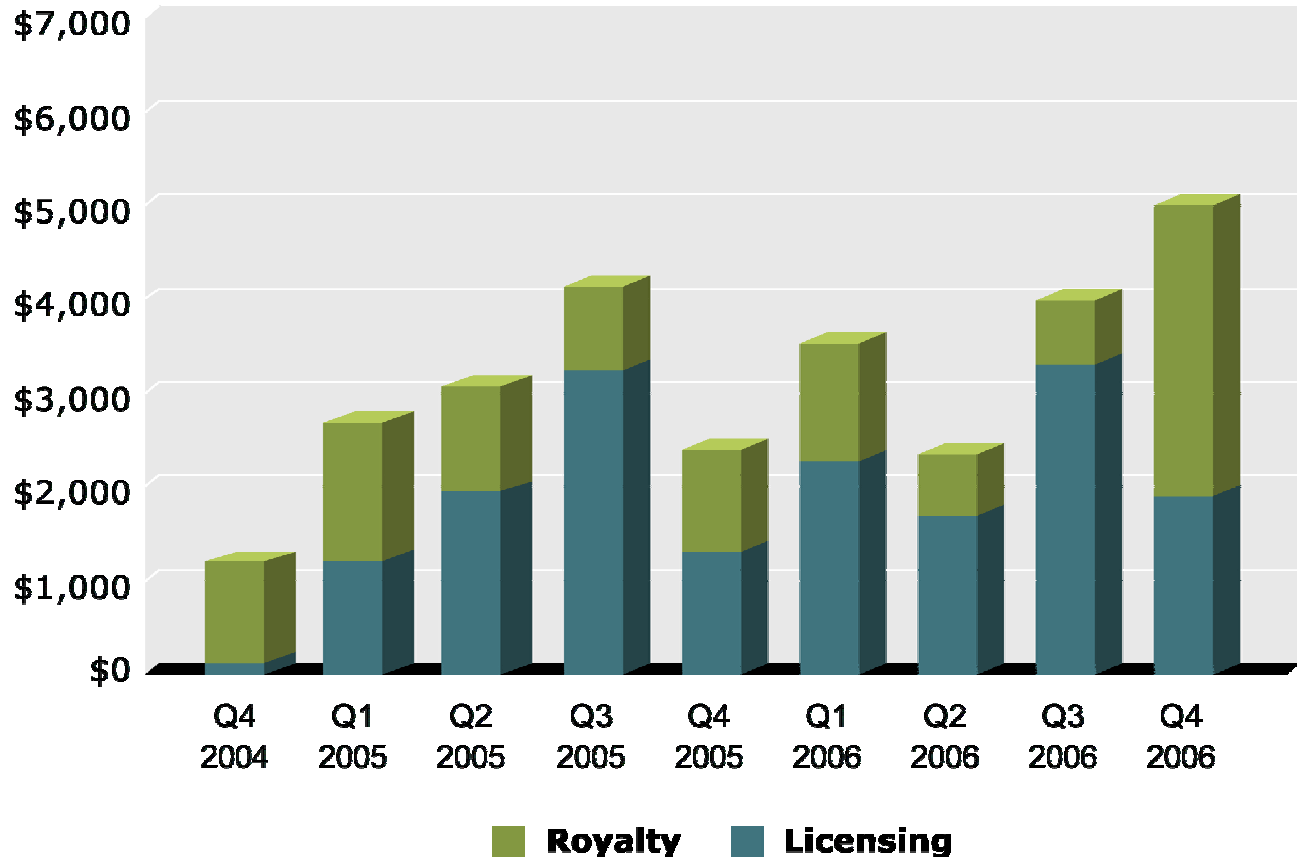


Sources of Revenue

Fabless Semi		IDM
2%-5% of Selling Price	Production Volume Based Royalties (Recognized when received)	2%-5% of Selling Price
10%-15% of Licensing Price	Maintenance And Support Fees (Annual)	10%-15% of Licensing Price
\$100K-\$300K Typical	Non-Recurring Engineering Fees (Recognized ratably or on milestones)	\$500K-\$1M Typical
\$250K-\$500K Per Macro	License Fee (Recognized when booked)	\$1.5M-\$3M Per License



MoSys Revenue History (000's)





MoSys: Strong Balance Sheet

As of December 31, 2006

Actual
(In thousands)

Cash, cash equivalents and investments	\$84,299
Total assets	\$103,760
Liabilities	\$2,845
Stockholders' equity	\$103,915
Debt	\$0



Key Drivers for Ongoing Success

- **Rich Technology Foundation:**
 - 1T-SRAM Compelling advantages at 65nm and beyond
 - 1T-FLASH Breakthrough technology for SoC code storage
- **Broad Customer Adoption:**
 - Multiple products
 - Multiple generations
 - Multiple markets
- **Proven Technology:**
 - >110 Million units shipped
 - >175 contracts executed

Thank you!

Chet Silvestri
President & CEO

