

MoSys

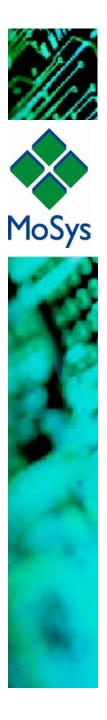
### Monolithic System Technology, Inc.

Corporate Presentation January 2006



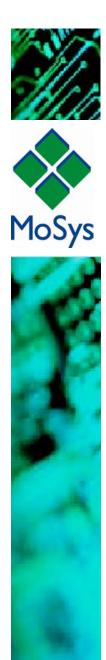
### Safe Harbor Statement

This presentation may contain forward-looking statements about the Company including, without limitation, benefits and performance expected from use of the Company's IT-SRAM<sup>®</sup> technologies.Forward-looking statements are based on certain assumptions and expectations of future events that are subject to risks and uncertainties. Actual results and trends may differ materially from historical results or those projected in any such forward-looking statements depending on a variety of factors. These factors include but are not limited to, customer acceptance of our IT-SRAM<sup>®</sup> technologies, the timing and nature of customer requests for our services under existing license agreements, the level of commercial success of licensees' products, ease of manufacturing and yields of devices incorporating our IT-SRAM<sup>®</sup>, our ability to enhance the IT-SRAM<sup>®</sup> technology or develop new technologies, the level of intellectual property protection provided by our patents, the vigor and growth of markets served by our licensees and customers, and operations of the Company and other risks identified in the Company's most recent annual report on Form 10-K filed with the Securities and Exchange Commission, as well as other reports that MoSys<sup>®</sup> files from time to time with the Securities and Exchange Commission. MoSys® undertakes no obligation to update publicly any forward-looking statement for any reason, except as required by law, even as new information becomes available or other events occur in the future.



### **MoSys<sup>®</sup> Milestones**

□ Founded: 1991 □ First IT- SRAM chip shipped: QI '99 □ 25th Patent Awarded: Dec '99 First Royalty Received: Q4 '00 □ Initial Public Offering (IPO): Apr '01 □ 100th Contract Signed: Nov '02 □ 75th Patent Awarded: Oct '04 □ Number of Customers: 52 Cumulative Units Shipped: 100,000,000



### Markets MoSys Serves



#### **Digital camcorders**



#### **Wireless applications**



LAN & WAN Switches



**Network Routers** 



**Consumer products** 

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**Digital cameras** 



**Cell phones** 



Video Game Consoles





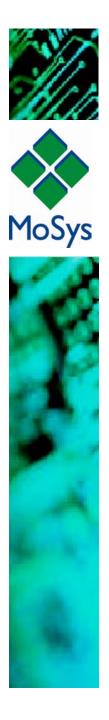
### SoC Manufacturing Challenge

System on Chip Memory Circuits Logic Circuits

Manufacturing Process Memory Process P

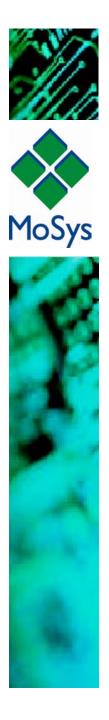
Logic Process

SoC Designers historically choose between Dense Logic or Dense Memory But not both

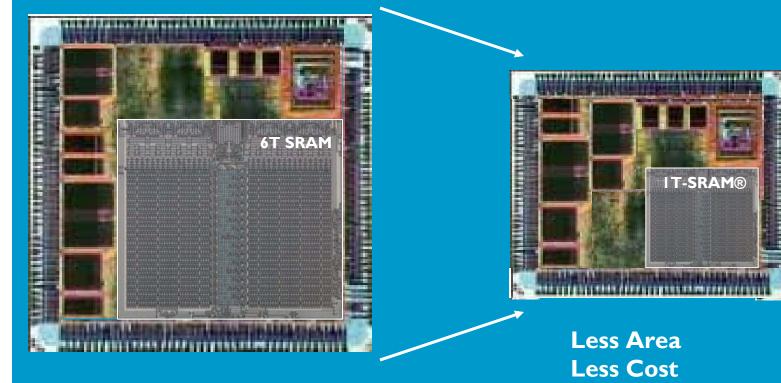


### **MoSys Technology**

MoSys enables low cost logic processes to be used for dense memory, enabling a breakthrough in cost, power consumption, and application flexibility

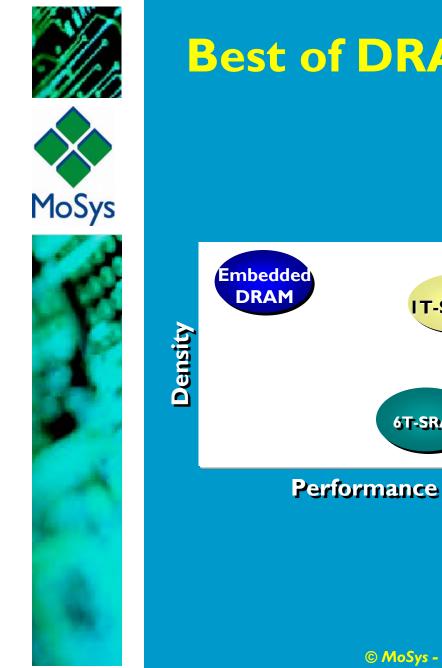


### **MoSys Technology**

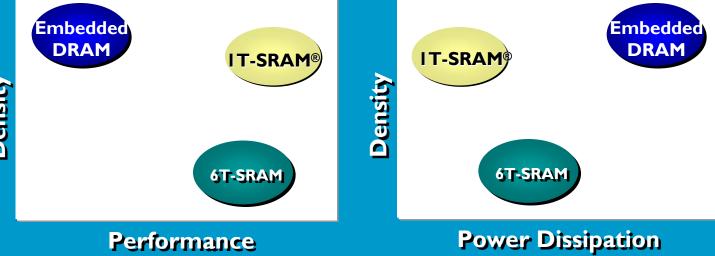


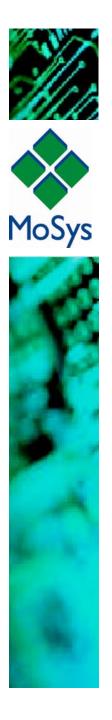
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**Less Power** 

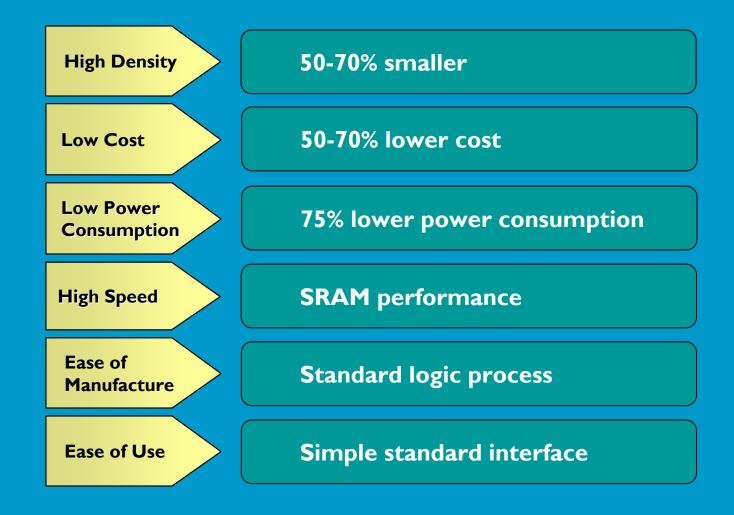


### **Best of DRAM and SRAM**





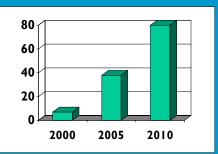
### **IT-SRAM<sup>®</sup> is Unmatched**



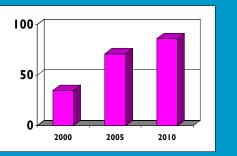


## **MoSys Market Opportunity**

#### SoC Market

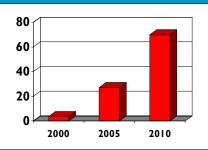


The SoC Market Expands to \$80B In 2010 (1) % of SoC Emb Mem



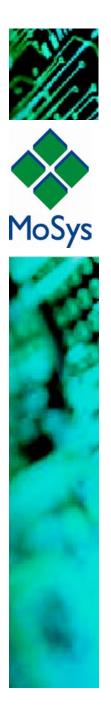
While the % of Embedded Memory On Chip Grows to 80% (2)

#### **MoSys TAM**

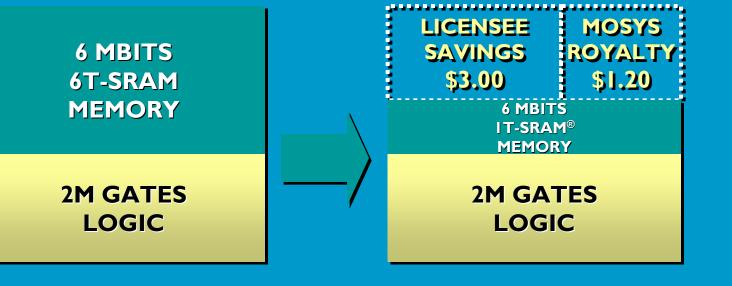


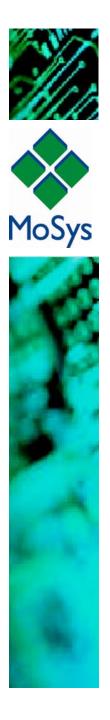
Creating a Total Available Market of More than \$50B (3)

Source: Business Communications Company, 2/03, 11/04 Source: SIA, ITRS 2003 The product of 1 \* 2



# **IT-SRAM<sup>®</sup> SOC Cost Savings**





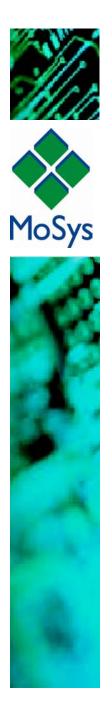
### **Our Technology is Protected**

**Proprietary, MoSys-invented technology** 

Protected by 80 U.S. and 39 foreign patents

**Difficult to replicate** 

MULTIBANK<sup>®</sup> Architecture Pipelined Self-timed Access Refresh Management Circuitry Leakage Suppression Circuitry TEC® Error Correction Circuitry



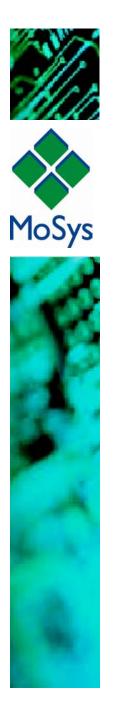
### **Revenue Stream**

□ License Fees
✓ \$250k to \$500k per macro

□ Customization ✓ \$100k to \$300k typical NRE

### Royalties

- ✓ 2% to 5% of SOC selling price
- $\checkmark$  All licensees pay royalties
- ✓ No upper limits



## **Historical Challenges**

Scuttled Synopsys transaction in 2004
 Changing strategy: Chips, Consulting, etc.
 Custom memory designs – difficult to scale
 Ineffective foundry strategy – small accounts



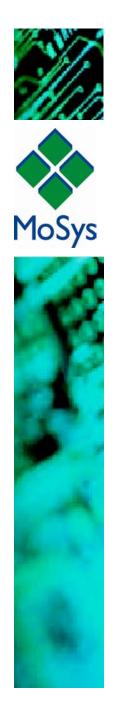
### **Key Drivers for Future Success**

CLASSIC Macros to better leverage development resources

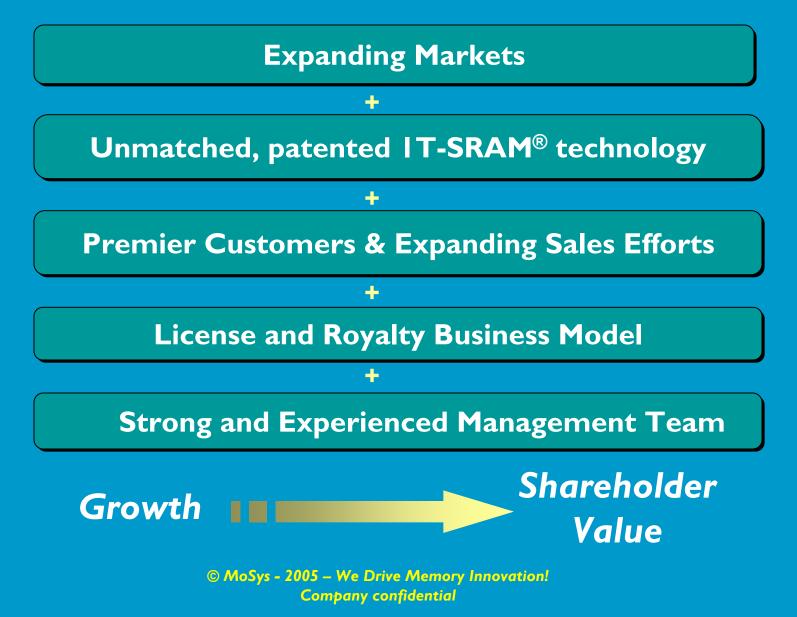
□ IT-SRAM ideally suited for 65 nm product transition

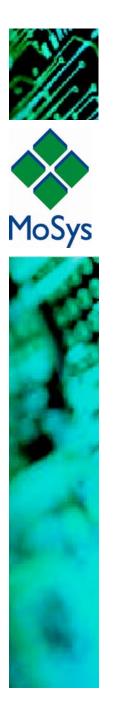
Expanded world-wide sales organization

Seasoned Management team



### **Investment Foundation**





# Thank you!

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