

Cavium Networks Announces Broad New Line of Storage Services Processors (SSPs)

New OCTEON™ Storage Services Processors Integrate 2 to 12 MIPS64 Cores, Storage and Network Interfaces, RAID Hardware Acceleration and Innovative RAS Features

MOUNTAIN VIEW, Calif., June 26, 2007 – Cavium Networks (NASDAQ: CAVM), a leading provider of highly integrated semiconductor products that enable intelligent processing for networking, communications, storage and security applications, today announced the introduction of the OCTEON MIPS64 Multicore Storage Services Processor (SSP) family of products. This family of products has been designed to address the specific needs of the Storage market. The OCTEON SSP single chip solutions combine innovative storage processing with Cavium's proven high performance networking, security and application acceleration processors. The OCTEON SSP delivers two to four times more compute cycles versus many alternatives and integrates advanced hardware acceleration for RAID, compression, de-duplication, security and iSCSI. The OCTEON SSP family scales from low-end solutions to very highperformance SAN/NAS systems, providing a flexible, software compatible platform across a broad range of price and performance points. Key OCTEON SSP family features include integration of 2 to 12 cnMIPS™ Plus 64-bit cores, storage application acceleration, an array of high-speed input/output interfaces including 10-Gigabit XAUI, PCI-Express, Gigabit Ethernet ports, dual DDR2 memory controllers, and innovative data protection mechanisms.

The OCTEON SSP CN57XX and CN55XX are targeted for applications such as Fiber Channel and Ethernet Disk Arrays, RAID controllers, multi-protocol switches, single-chip iSCSI adaptors, virtualization equipment, Protection and NAS/SAN equipment.

"Storage infrastructure is going through significant changes that include data consolidation driven by regulatory compliance, I/O virtualization, storage archival on disk, and increasing storage access over Wide Area Networks," said Aileen A Arcilla, IDC's Senior Analyst for Semiconductors: Networking, Broadband and Storage. "These changes are driving the need for intelligent processing, higher capacity, reliability, security and data compression in next generation storage solutions."

"Existing storage systems implement different hardware and software architectures across the low-end, mid-range and high-end performance and capacity points," said Syed Ali, President and CEO at Cavium Networks. "Cavium's SSP now offers a scalable processor family that enables common architecture across a broad range of performance and capacity points with essential integrated hardware acceleration for RAID 5/6, compression, de-duplication and security."

Current solutions often require multiple chips to perform storage input and output processing, RAID processing and services. These storage systems typically report simple IOPS and raw throughput without reference to full featured performance since performance drops when services are enabled. With the new storage services requirements of 10 Gigabit Ethernet, security, compression, de-duplication, and virtualization gaining traction, full featured performance metrics are becoming important and are forcing OEMs to add new acceleration chips to their storage systems. The new OCTEON SSP family significantly reduces the number of chips required to build full featured next generation storage systems by integrating multiple functions into a single chip.

OCTEON Storage Services Processors

The OCTEON SSP processor family consists of seven different software-compatible products with 2 to 12 cnMIPS™ Plus cores, with clock speeds of up to 1 Gigahertz which provides up to 12 GHz of 64-bit processing and 2MB of on-chip L2 Cache. Additionally, OCTEON SSP processors incorporate advanced storage acceleration for RAID5 and RAID6, TCP, data de-duplication, virtualization and compression/decompression. The OCTEON SSP Processor family integrates required interfaces for networking and disk connectivity with up to two 10-Gigabit XAUIs, two 8-lane PCIe controllers, eight SGMII Gigabit Ethernet ports and memory I/Os with dual 72-bit DDR2 controllers. OCTEON SSP processors are also available with integrated cryptographic offload including AES-GCM, XTS-AES with flexibility to support evolving storage encryption standards. OCTEON SSP also includes

innovative and flexible data protection and memory backup features to enable enterprise-class reliability, availability and serviceability.

Performance and Software

The OCTEON SSP processor family offers two to four times more compute cycles as compared to alternative solutions. OCTEON SSP enables storage equipment vendors to build low to high-end scalable storage solutions providing up to 3 Million on-chip virtual IOPS, 10Gbps of iSCSI, 1GB/s of compression / decompression, up to 2GB/s of in-line encryption in a single chip and up to 10GB/s of RAID acceleration. Cavium Networks provides a feature rich software development kit with 12-way SMP Linux 2.6, GNU Toolchain, debugger tools, RAID acceleration APIs and production ready software toolkits for a range of storage protocols. For more information about OCTEON Processors please visit: http://www.caviumnetworks.com/OCTEON_MIPS64.html.

Pricing and Availability

Pricing for the OCTEON MIPS64 Multicore SSP family ranges from \$59 to \$575, in 10K unit quantities. The OCTEON Development Kit using Simulator, tool-chain, reference applications and Storage software stacks are available now to partner companies. OCTEON™ CN57XX family processors with 6 to 12-core versions and evaluation boards will be available in the third quarter of 2007 and CN55XX family processors with 2 to 6-core versions and evaluation boards will be available in the fourth quarter of 2007.

About Cavium Networks

Cavium Networks is a leading provider of highly integrated semiconductor products that enable intelligent processing in networking, communications, storage and security applications. Cavium Networks offers a broad portfolio of integrated, software compatible processors ranging in performance from 10 Mbps to 10 Gbps that enable secure, intelligent functionality in enterprise, data-center, broadband/consumer and access & service provider equipment. Cavium Networks processors are supported by ecosystem partners that provide operating systems, tool support, reference designs and other services. Cavium Networks principal offices are in Mountain View, CA with design team locations in California, Massachusetts and India. For more information, please visit: <http://www.caviumnetworks.com>

This press release may contain forward-looking statements regarding future events that involve risks and uncertainties. Readers are cautioned that these forward-looking statements are only predictions and may differ materially from actual future events or results. These forward-looking statements involve risks and uncertainties, as well as assumptions that if they do not fully materialize or prove incorrect, could cause our results to differ materially from those expressed or implied by such forward-looking statements. The risks and uncertainties that could cause our results to differ materially from those expressed or implied by such forward-looking statements include but are not limited to, the performance levels of the OCTEON SSP processor products, availability of OCTEON™ CN57XX family processors and evaluation boards in the third quarter of 2007 and CN55XX family processors and evaluation boards in the fourth quarter of 2007, the potential design of these products into Fiber Channel and Ethernet Disk Arrays, RAID controllers, Multi-protocol switches, single-chip iSCSI adaptors, Virtualization equipment, Protection and NAS/SAN and other equipment by leading or other Storage equipment vendors and other risks and uncertainties described more fully in our documents filed with or furnished to the Securities and Exchange Commission. More information about these and other risks that may impact Cavium's business are set forth in our Registration Statement on Form S-1 filed with the SEC, including the "Risk Factors" section in our final Prospectus dated May 1, 2007. All forward-looking statements in this press release are based on information available to us as of the date hereof and qualified in their entirety by this cautionary statement, and we assume no obligation to revise or update these forward-looking statements to reflect events or circumstances after the date hereof.

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