Corporate Overview

XILINX®

Safe Harbor Disclaimer

During the course of this presentation, we may provide projections or other forward-looking statements regarding future events and/or future financial performance. We wish to caution you that such statements are just predictions and that actual events or results may differ materially. We refer you to the documents the Company files from time to time with the Securities and Exchange Commission. Specifically, we refer you to the Company's last filed Forms 10-K and/or 10-Q. These documents contain and identify important factors that could cause the actual results to differ materially from those contained in our projections and other forward-looking statements.

Xilinx At-A-Glance

- Inventor of the FPGA (Field Programmable Gate Array)
- Pioneer of the Fabless Semiconductor Model
- Founded: 1984
- Public: 1990
- NASDAQ: XLNX
- Corporate Headquarters in San Jose, CA
- Approximately 3,400 Employees Worldwide
- More than 21,000 Customers Worldwide

What is a PLD?

- A Programmable Logic Device (PLD) is a circuit which can be configured by the user to perform a logic function

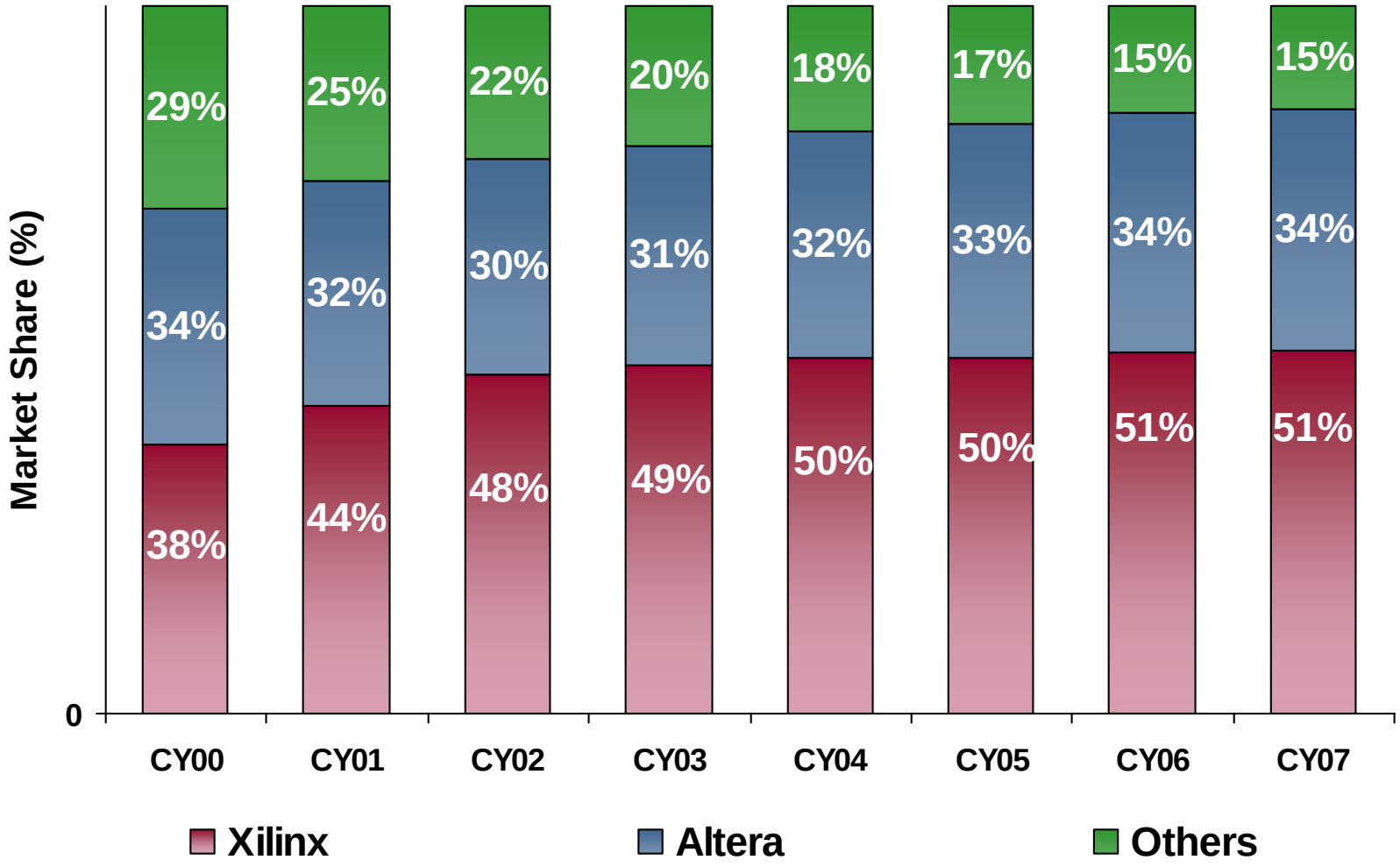
- Xilinx designs, develops and markets two main types of programmable logic devices:
  - Field Programmable Gate Arrays (FPGAs)
  - Complex Programmable Logic Devices (CPLDs)

Why Programmable Logic?

Xilinx provides standard parts…

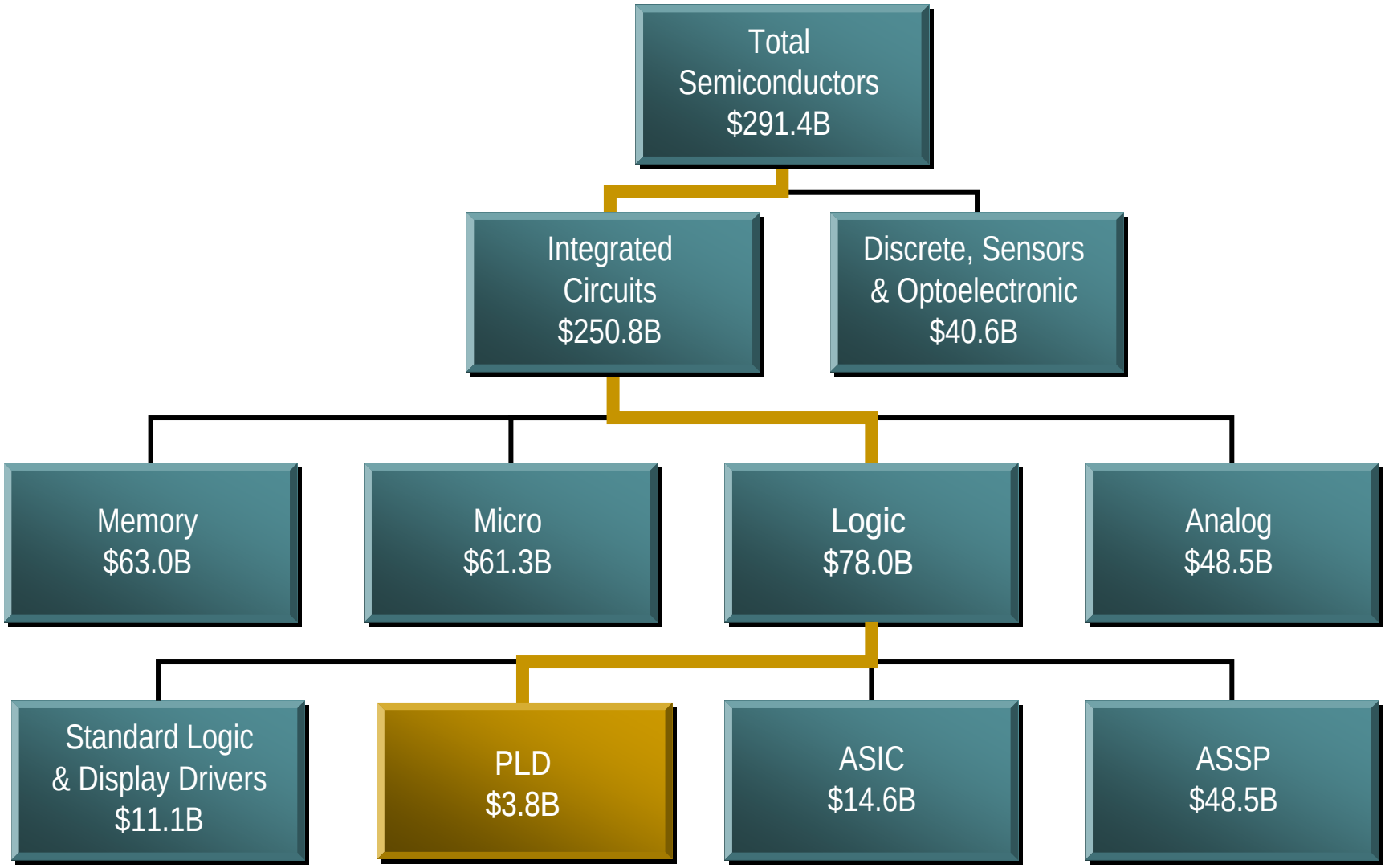
- Faster time to market and volume production
- Modifications made quickly through Xilinx software
- Low inventory risk for customers
- Field Programmability
- No up front NRE (Non Recurring Engineering)

PLD Market Segment Share



Source: iSuppli

Worldwide Semiconductor Market 2008

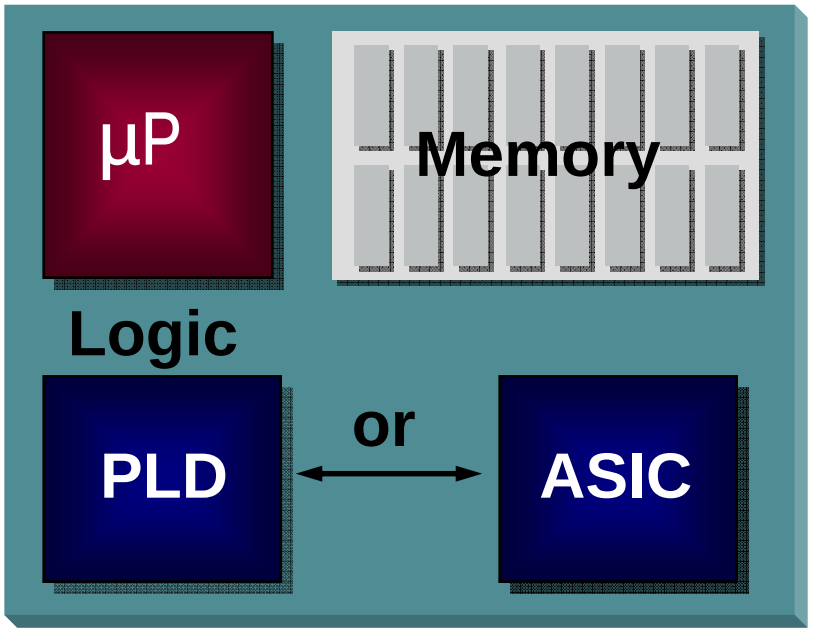


 Source: iSuppli, December 2007

Where Xilinx Fits

Key components of an electronics system…

- Processor
- Memory
- Logic



Xilinx is the Leading Supplier of Programmable Logic Devices

End Market Applications

Communications

- Routers
- Switches
- Wireless Base Stations

Industrial & Other

- Factory Automation
- Medical Imaging
- Test & Measurement
- Aerospace & Defense

Consumer & Auto

- Plasma Displays
- Set Top Boxes
- GPS Navigation Systems

Data Processing

- Servers
- Computer Peripherals
- Storage Area Networks

Broad Customer Base

Xilinx Products At-A-Glance

Virtex™-5 FPGA Series
The Ultimate System Integration Platform

Spartan™-3 Generation FPGAs
World´s Lowest Cost FPGAs

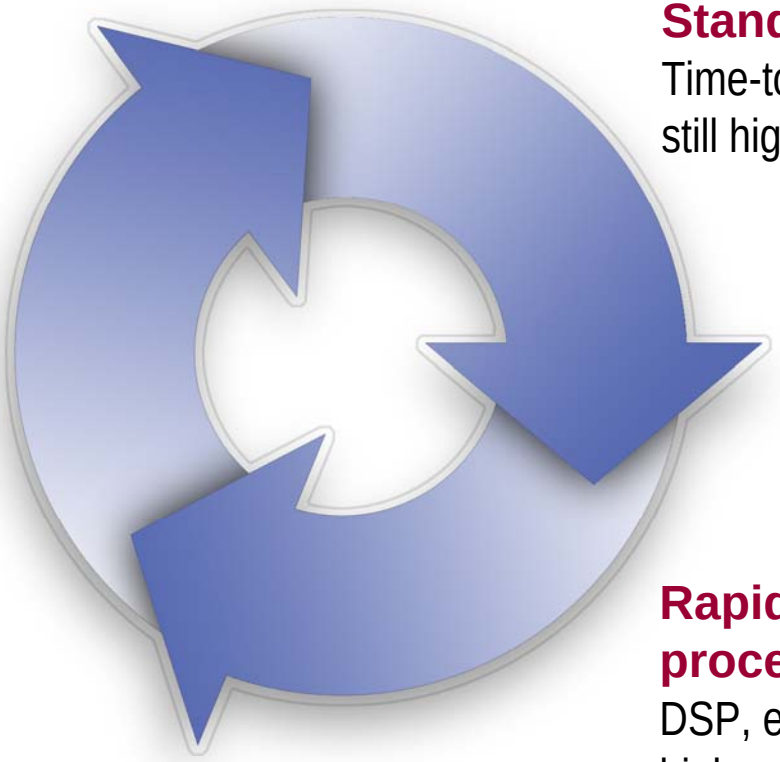
CoolRunner™-II
World´s Lowest Cost, Lowest Power CPLDs

Changing Competitive Dynamics

Changing Standards
Time-to-market and flexibility still highly valued

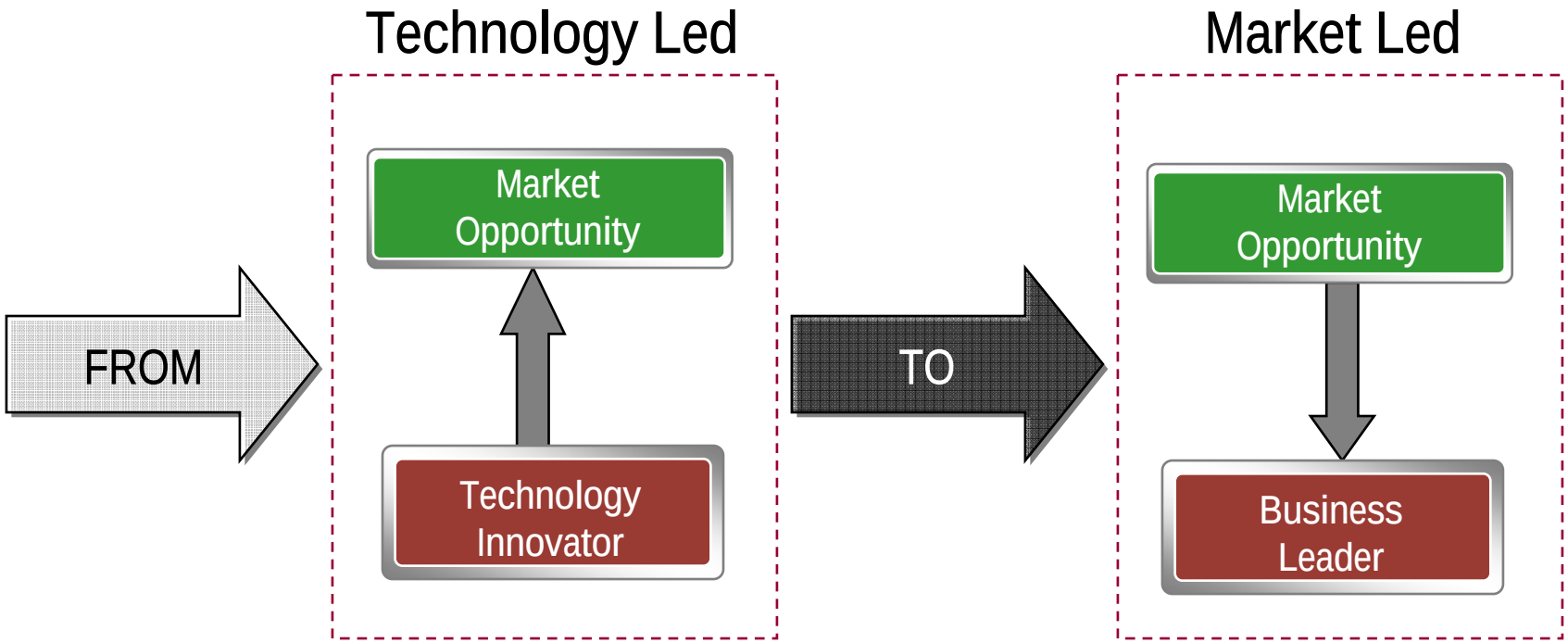
Changing Economics
Moore's Law becoming more difficult and expensive

PLDs & ASSPs (mainstream)
standard cell ASICs (niche)
structured ASICs (failing)

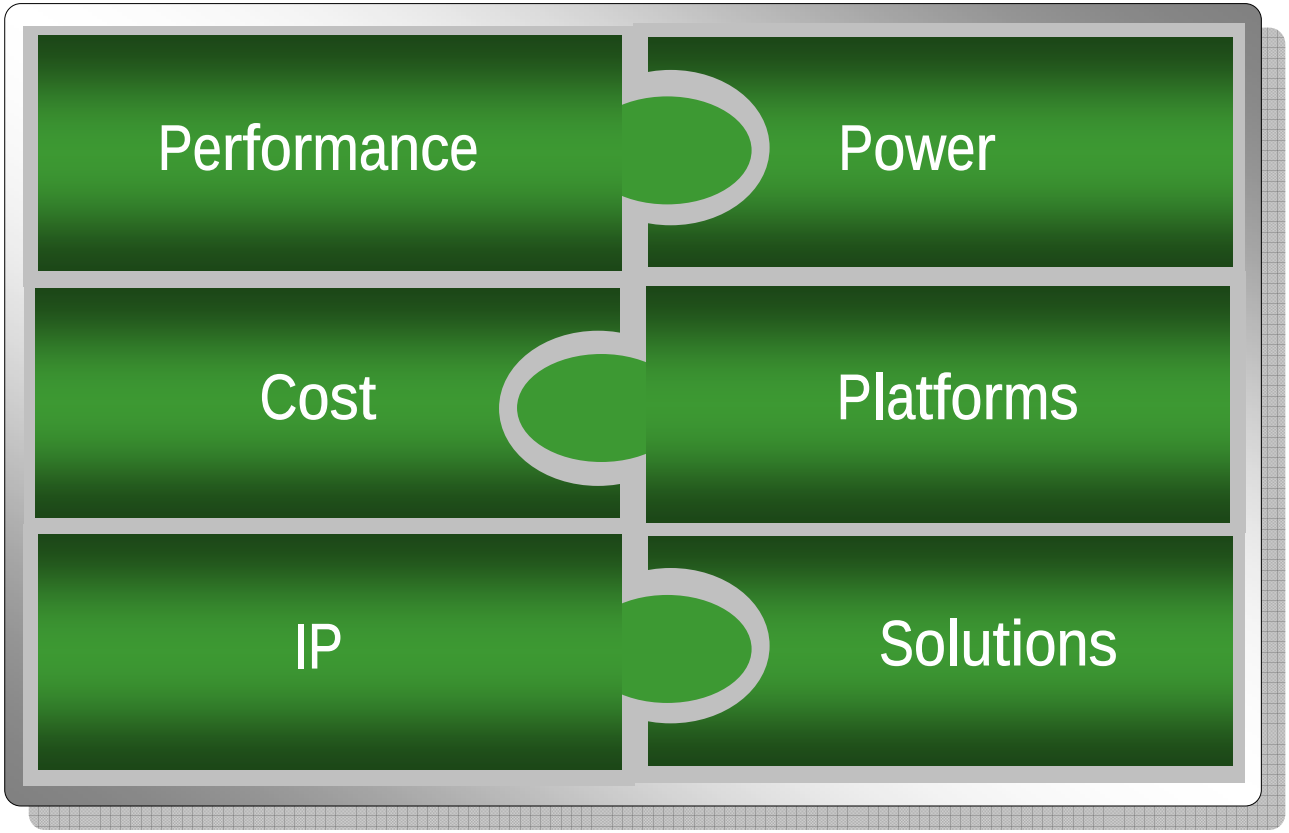


Rapidly increasing processing needs
DSP, embedded processing, high speed I/O becoming significant factors

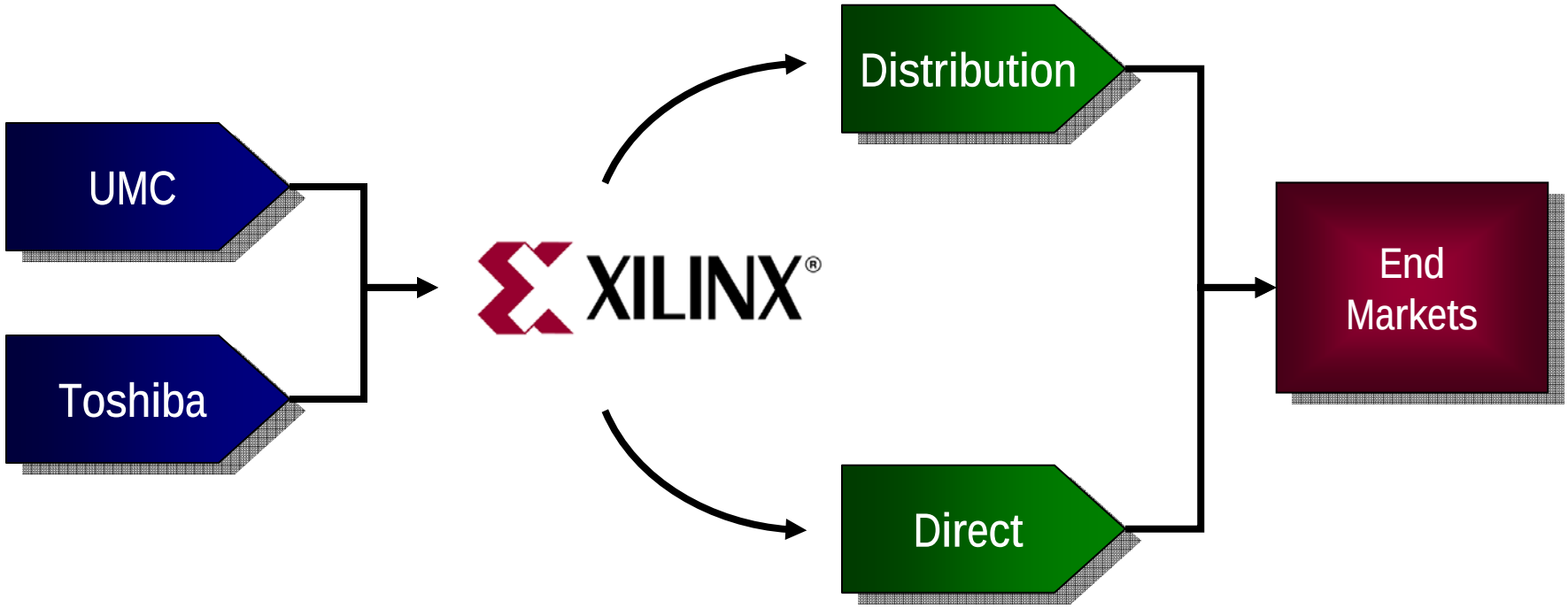
Technology Leader to Business Leader



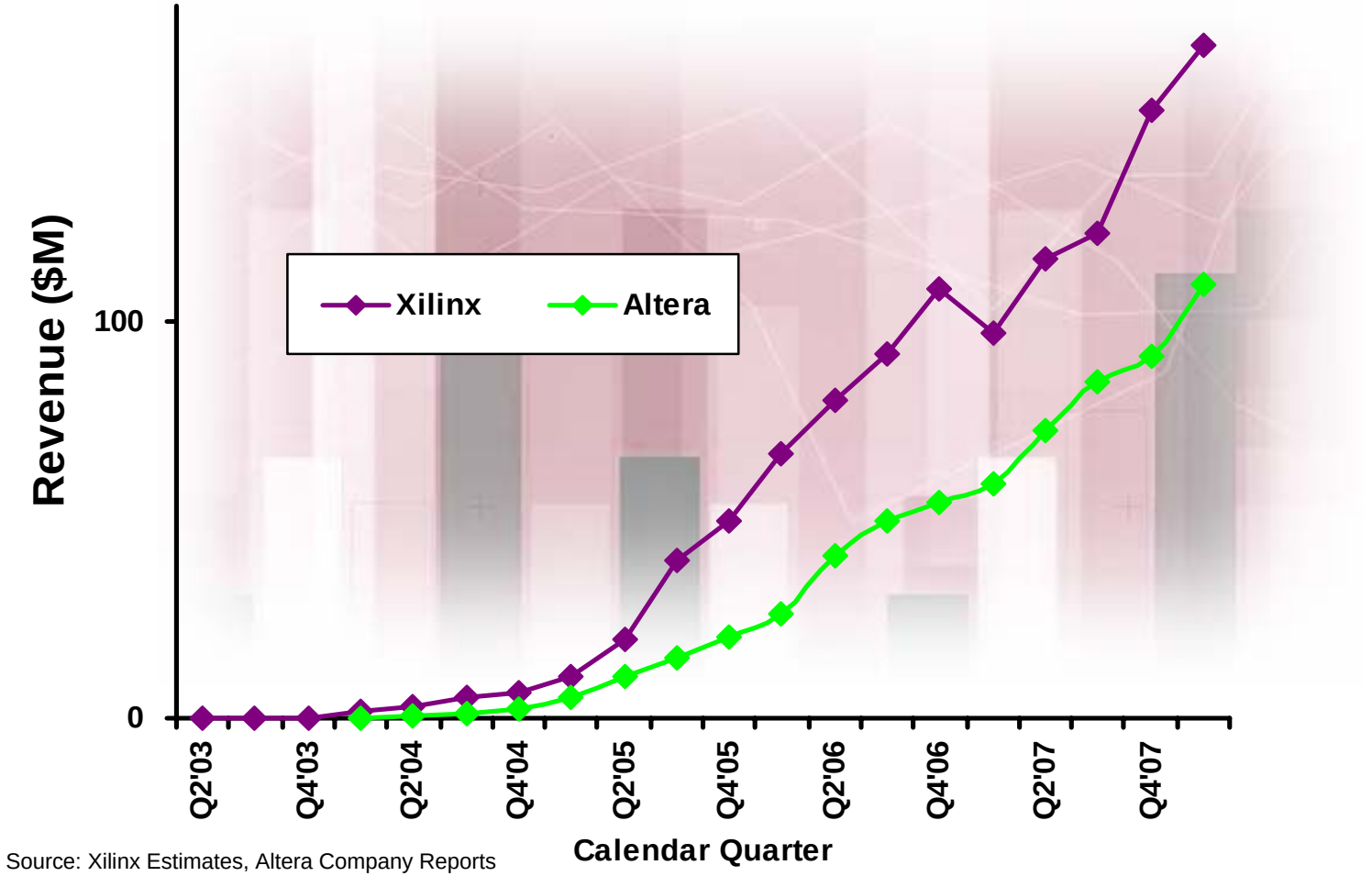
New Capabilities to Drive Growth



Xilinx Supply Chain

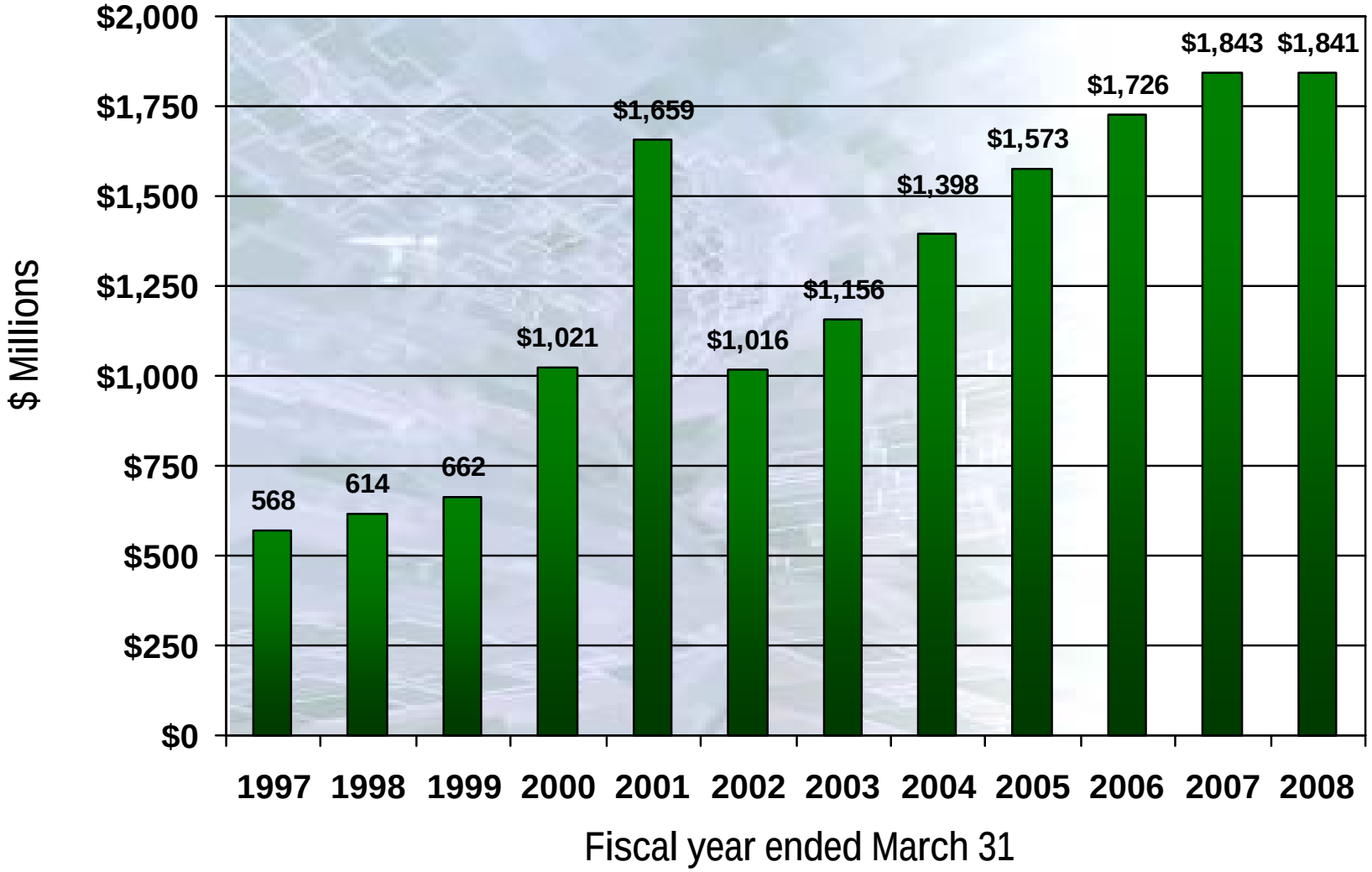


Process Technology Leadership Sales From 90nm & 65nm Nodes



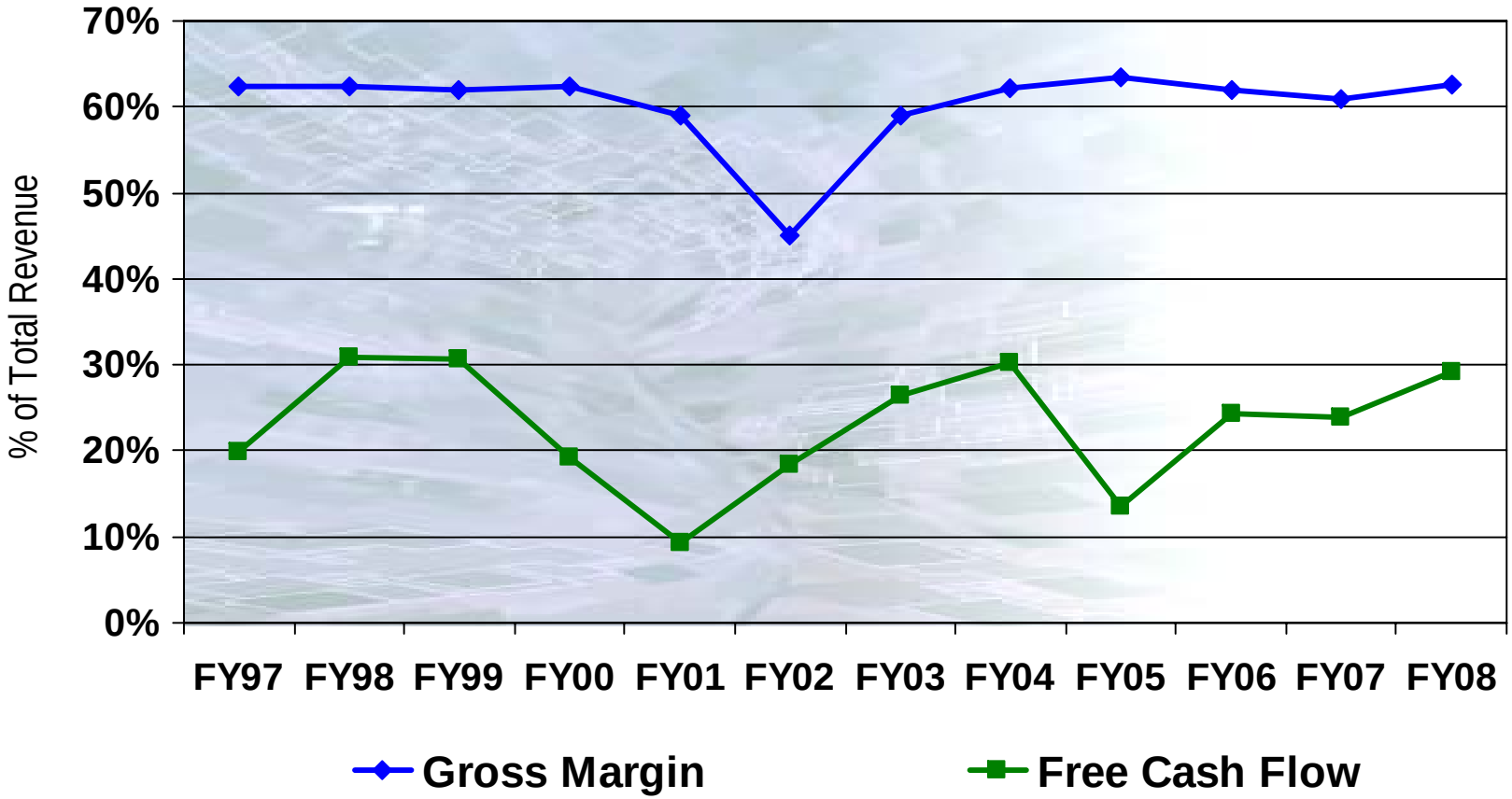
Source: Xilinx Estimates, Altera Company Reports
Note: Xilinx 90/65nm products include Spartan 3/3E/3A/N and Virtex-4/5
Altera 90/65nm products include Arria GX, Cyclone-II/III and Stratix-II/III

Xilinx Historical Revenue



Historical Gross Margin & FCF

- Xilinx Has a Strong and Stable Business Model
- High Gross Margins (typically ~60%) and Strong Free Cash Flow



Xilinx Operating Targets

	Prior Target	New Target
Gross Margin	60% - 62%	63% - 65%
R&D	~19%	17%
SG&A	~18%	17%
Operating Margin	24%-26%	30%
ROE	N/A	>30%

Summary

- Reigniting Growth in a Disciplined Financial Framework
- End Market Requirements Driving Programmability