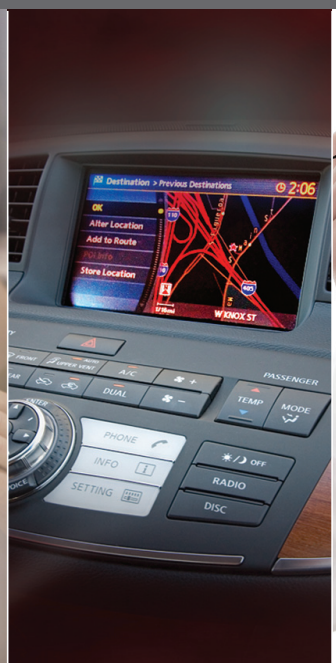
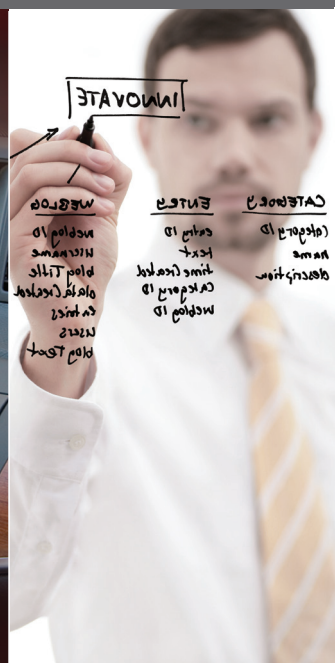
Corporate Fact Sheet







XILINX®

Xilinx is the world's leading provider of programmable platforms, with $1.7B in revenues in 2009 and more than 50 percent market share in the programmable logic device (PLD) segment of the semiconductor industry.

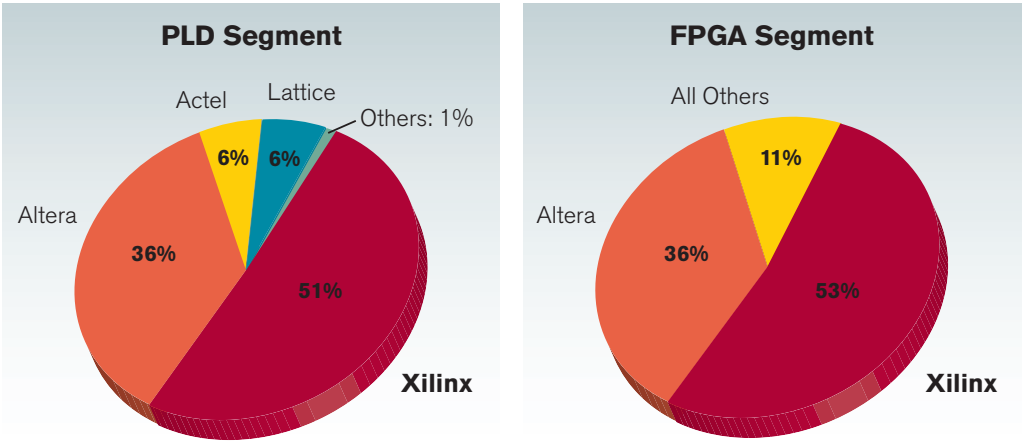
Xilinx programmable chips are the innovation platform of choice for today's leading companies for the design of tens of thousands of products that improve the quality of our everyday lives. Due to their inherent flexibility, Xilinx award-winning programmable solutions – silicon, software, IP, evaluation kits and reference designs – are used by more than 20,000 customers to:

- Deliver innovative new products to market in a matter of weeks
- Drastically reduce research and development costs
- Change or upgrade end product features and functions "on the fly" to meet new market demands and adapt to changing industry standards

Among the end markets Xilinx serves are: aerospace and defense, automotive, consumer, industrial, medical, scientific, wired and wireless communications. For example, Xilinx chips are designed into automotive navigation systems and rear-seat displays, ultrasound imaging systems, IT gear for wireless computing and mobile applications, flat panel televisions, and sophisticated mobile communications systems used on the networked battlefield. They're even on board the Mars Rover space mission!

Programmable Device Market Segment Share

Q3 Calendar Year 2009



Xilinx revenues are greater than all other pure-play PLD companies combined.

Source: Company reports
Latest information available; computed on a 4-quarter rolling basis

XILINX®

Xilinx Facts At-a-Glance

- Founded 1984
- ~3,000 employees worldwide
- 20,000 customers worldwide
- 2,000+ patents
- Inventor of the field programmable gate array (FPGA)
- Pioneer of fabless manufacturing model
- Headquarters in San Jose, Dublin and Singapore
- Publicly traded company since 1990 (NASDAQ: XLNX)
- $1.7B in revenues in calendar year 2009

Executive Team

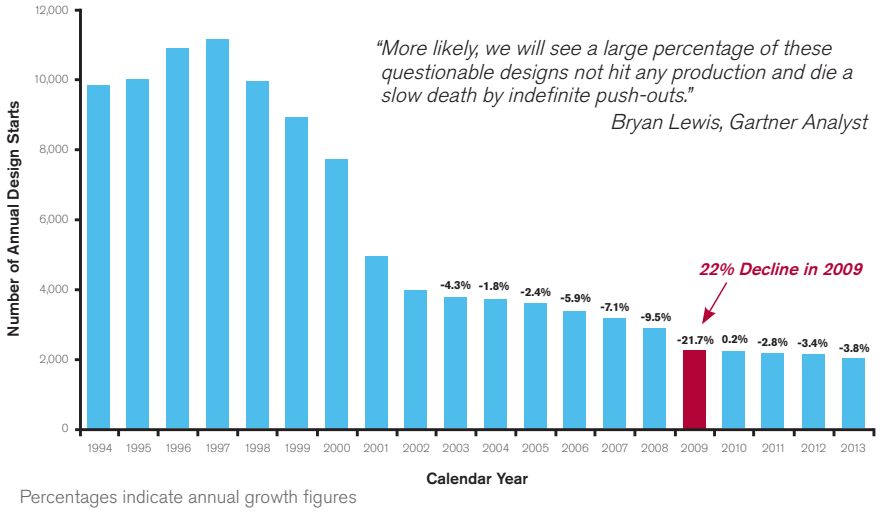
- Moshe Gavrielov, President & Chief Executive Officer
- Ivo Bolsens, Senior Vice President and Chief Technology Officer
- Kathy Borneman, Corporate Vice President, Worldwide Human Resources
- Kevin Cooney, Corporate Vice President and Chief Information Officer
- Scott Hover-Smoot, Corporate Vice President, General Counsel and Secretary
- Jon Olson, Senior Vice President and Chief Financial Officer
- Victor Peng, Senior Vice President, Programmable Platforms Development
- Vin Ratford, Senior Vice President, Worldwide Marketing
- Vincent Tong, Senior Vice President, Worldwide Quality and New Product Introductions
- Frank Tornaghi, Senior Vice President, Worldwide Sales
- Krishna Rangasayee, Vice President, Corporate Strategic Planning

Today's Global Economics Favor Programmable Chips

More than at any other time, global economics favor programmable chips over costly application-specific integrated circuits (ASICs) and application-specific standard products (ASSPs).

Application-specific devices can take 12 to 24 months to design, and the fixed costs associated with semiconductor manufacturing have risen to an exorbitant $60 million on the most advanced 40nm chip-making technology from $20 million on 90nm technology, just three years ago. Even the tiniest mistake in implementation can result in a multimillion dollar mask re-spin, leading to time-to-market delays and a potentially devastating "domino effect" on a company's business.

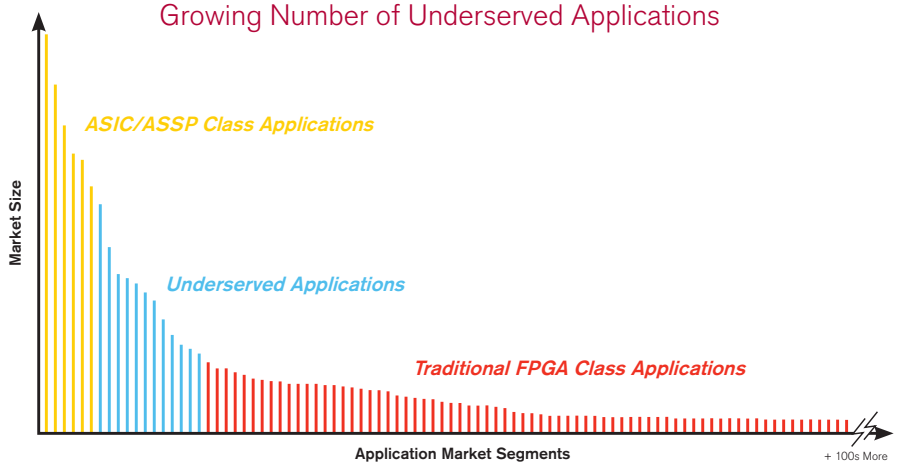
ASIC Landscape Continues to Deteriorate:



Percentages indicate annual growth figures
Source: Gartner (March 2009)

ASIC/ASSP Application Gap is Growing

Growing Number of Underserved Applications



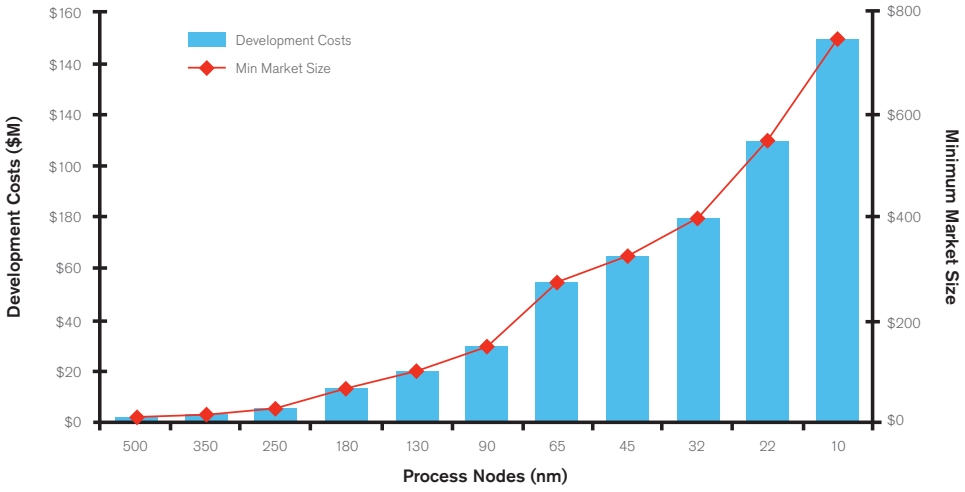
XILINX®

In contrast, programmable chips – also known as field programmable gate arrays – are chameleon-like, making them imminently suitable for a wide range of applications in diverse markets. Simply put, Xilinx offers the design engineer a "blank" device that can be configured (and reconfigured) to implement any logic function that can be performed by an application-specific device. Hundreds of thousands of programmable logic blocks, made up of billions of transistors, are available to the designer to "wire together" using reconfigurable interconnects to execute myriad electronic functions.

Increasingly, customers cite flexibility as the number one reason they choose FPGAs over application-specific and even standard devices. They can make changes to their designs very late in the design cycle – even after the end product has been manufactured and deployed in the field. They can also extend the life of a product by reprogramming the chip. What's more, they can upgrade end products remotely, eliminating the costs associated with re-designing or manually updating electronic systems.

In today's challenging economic climate, programmable platforms have become strategically essential for world-class system companies to effectively compete. The costs and risks associated with application-specific devices can only be justified for a short list of ultra-high volume commodity products.

High Development Costs Driving ASSPs to Ultra-High Volume Markets



Source: Xilinx, Venture Capital Insights

The Programmable Imperative

Altogether, market forces have converged to create a new era of electronics in which programmability is an imperative. In essence, programmable platforms have become the only viable means for today's companies to meet increasingly stringent product requirements – cost, power, performance, and density – in a business environment characterized by spiraling complexity, shrinking market windows, fickle market demands, capped engineering budgets, escalating ASIC and ASSP non-recurring engineering costs, and increased risk.

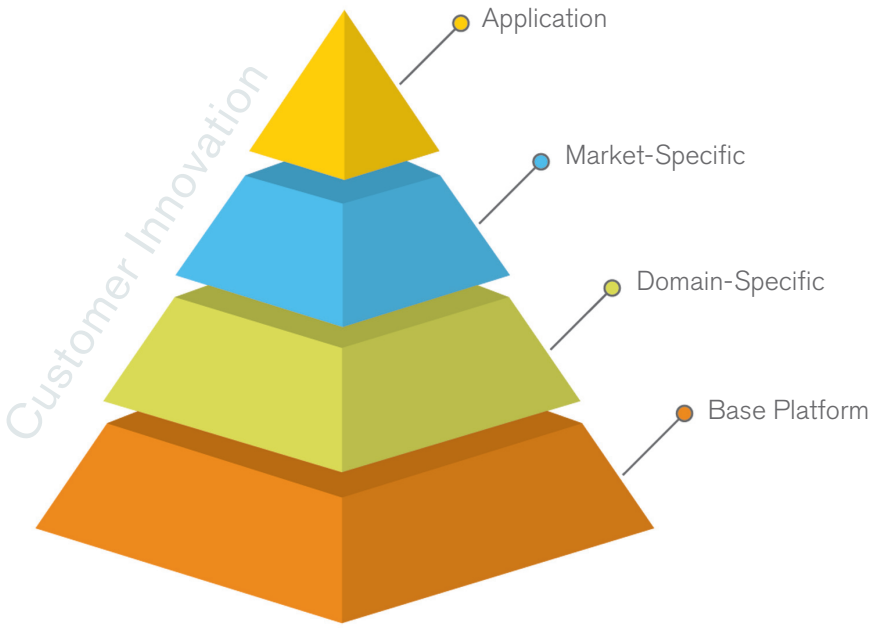
For Xilinx, the programmable imperative represents a two-fold commitment. First, to increase performance, densities and system-level functionality, while driving down cost and power consumption, at each manufacturing process node with every new generation of FPGAs. Secondly, to provide simpler, smarter programmable platforms and design methodologies that free up engineers to focus on end product innovation and differentiation.

In 2009, Xilinx introduced the Virtex®-6 family of 40nm FPGAs for compute intensive, high-speed, high-density SoC applications, and the Spartan®-6 family of 45nm FPGAs for applications where size, power, and cost are key considerations. Notably, sales of our 40/40nm platforms have ramped quickly since their introduction earlier this year and are expected to exceed $10M by fiscal year end, in April 2010.

Each family serves as the foundation for programmable targeted design platforms that enable software and hardware designers alike to leverage open standards, common design methodologies, development tools, and run-time platforms. Engineers can pick and choose from a variety of options to jumpstart their designs, freeing them up to focus design talent on end product innovation and differentiation.

Among the platform options are: base (silicon, IP, logic tools, boards, reference designs), domain (embedded processing, DSP or logic/connectivity IP & tools, FMC daughter cards), and market (IP, custom tools & boards).

Targeted Design Platforms



Xilinx targeted design platforms integrate five key elements, delivered by Xilinx and its robust network of ecosystem partners, and supported by field applications engineering and design services teams with in-depth expertise in domain and market-specific applications.

- Xilinx Virtex-6 and Spartan-6 FPGAs www.xilinx.com/6
- Design environments supporting and integrating industry-proven methodologies www.xilinx.com/designtools
- Scalable boards and kits adopting the industry standard FPGA mezzanine connector (FMC) www.xilinx.com/kits
- Socketable IP cores www.xilinx.com/ipcenter
- Robust reference designs www.xilinx.com/kits

A Legacy of Leadership & Innovation

Since its founding more than 25 years ago, Xilinx has driven wave after wave of innovation in the semiconductor industry, pioneering the fabless manufacturing model, inventing the FPGA, and building a company based upon a core set of values that have stood the test of time.

Xilinx has been recognized worldwide for its business, market and technology leadership – among the most recent awards and certifications the company has earned are:

Customer Satisfaction & Quality Awards

- Brocade, Quality Excellence Award, 2009
- General Dynamics, Strategic Supplier Award, 2008
- Harmon Becker, Top 10 Percent Semiconductor Supplier Award, 2008
- Huawei Core Partner & Most Valued Supplier Awards, 2008, Gold Supplier Award, 2009
- Motorola, Preferred Supplier Award, 2009
- Sony, Quality Award, 2008
- Spirent Supplier of the Year, 2008
- Tellabs, Perfect 20/20 Quality Score, 2008
- ZTE Best Supplier Award, 2008-2009

Company Awards

- National Inventors Hall of Fame Induction, Invention of FPGA (Founder Ross Freeman), 2009
- Most Respected Public Fabless Company, Global Semiconductor Alliance, 2008
- 100 Best Corporate Citizens, CRO/Business Ethics Magazine, 2007-2009
- "25 Microchips that Shook the World" – Invention of FPGA, IEEE Spectrum, 2009
- Green Mark Platinum Award - Asia Pacific HQ, Singapore Ministry of National Development, 2007
- LEED Green Building Certification - Xilinx North America HQ, US Green Building Council, 2007

Product & Technology Awards

- Best FPGA Product Award – Virtex-6 and Spartan-6 Targeted Design Platforms, China Electronics News, 2009
- Innovation of the Year, Virtex-6 and Spartan-6 Targeted Design Platforms, EDN Magazine China, 2009
- Innovative Low Power Design Award – Virtex-6 and Spartan-6 FPGA Platforms, Chinese Institute of Electronics, 2009
- Influential Embedded Systems Award, Best New Concept – Virtex-6 and Spartan-6 Targeted Design Platforms, EEPW, 2009
- DesignVision Award – Virtex-5 TXT, International Engineering Consortium, DesignCon 2009
- ALICE Industrial Collaboration Award, CERN, 2008
- Best Product of the Year & Leading Product of the Year – Virtex-5 FXT FPGA Platform and ISE 10.1 Design Tools, EDN Magazine China, 2008
- Editor's Choice Award - Spartan-3A FPGA, Portable Design Magazine, 2008
- Top 10 China Influential Embedded Systems Award - Virtex-5 FPGAs, EEPW China, 2007
- Digital IC Product of the Year - Virtex-5 LXT FPGAs, EDN China, 2007
- Semiconductor Product of the Year - Virtex-5 LXT FPGA Platform, Elektra Awards, 2007
- Best of 2007 EDA/FPGA Tool – ISE 9.1i Design Tools, Electronic Design, 2007
- Design Vision Award - PlanAhead 8.2 Software, International Engineering Consortium, DesignCon 2007
- Product & Innovation of the Year - Virtex-5 FPGA, EDN Magazine, 2007
- Design Team of the Year - Virtex-5 FPGA Design Team, EDN Magazine, 2007
- Product of the Year - Virtex-5 FPGA Platform, EE Times China, 2007
- Semiconductor Product of the Year, Virtex-5 LXT FPGA Platform, Elektra Awards, 2007
- Most Innovative Product of the Year - Virtex-5 FPGA, Electronique Magazine, 2007

Quality Certifications

- OHSAS 18001 International Health & Safety Certification – San Jose, Dublin, Singapore, certified since 2008
- TS16949 International Automotive Quality System Specification – Hyderabad, India, certified since 2008
- ISO9001 Quality Management System – San Jose; original certification in 1995
- ISO9001:2000 Quality Management System – Xilinx Design Services, Dublin, San Jose, Colorado, and Dallas; certified since 2005
- ISO9001:2000 Quality Management System – Colorado; certified since 2004
- ISO/TS 16949:2002 Quality Management System – San Jose, Dublin, Singapore, Albuquerque, Hyderabad; certified since 2005
- ISO9001:2000 /TL 9000 Quality Management System – San Jose, Dublin, Singapore, Albuquerque; certified since 2004
- ISO14001:2004 Environmental Management System – San Jose, Dublin, Singapore; certified since 2004
- OHSAS 18001:2007 International Health & Safety Certification – San Jose, Dublin, Singapore; certified since 2008

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